

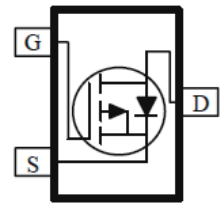
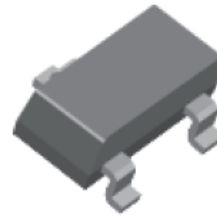
P-Channel 30-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits



PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
-30	57 @ $V_{GS} = -4.5V$	-3.9
	89 @ $V_{GS} = -2.5V$	-3.2

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 8	
Continuous Drain Current ^a	$T_A = 25^\circ C$	I_D	-3.9	A
	$T_A = 70^\circ C$		-3.1	
Pulsed Drain Current ^b		I_{DM}	-10	A
Continuous Source Current (Diode Conduction) ^a		I_S	-1.7	
Power Dissipation ^a	$T_A = 25^\circ C$	P_D	1.3	W
	$T_A = 70^\circ C$		0.8	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ C$

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	$R_{\theta JA}$	100	$^\circ C/W$
	Steady State		166	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

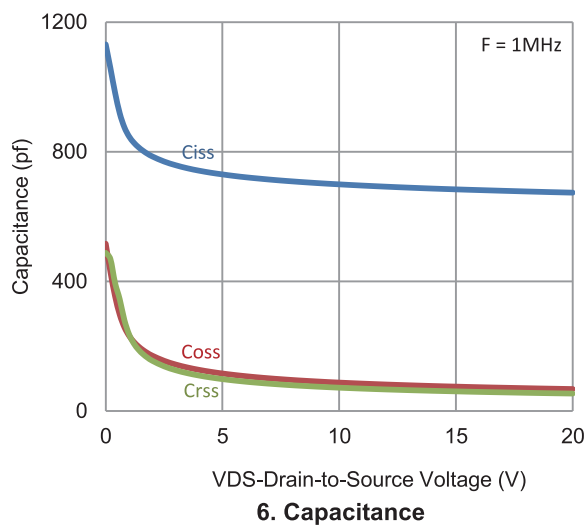
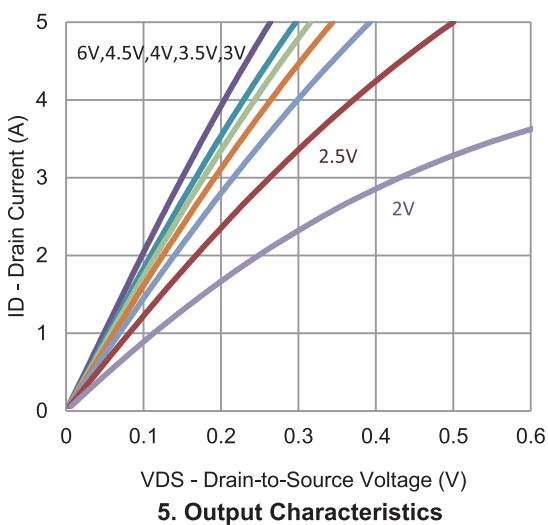
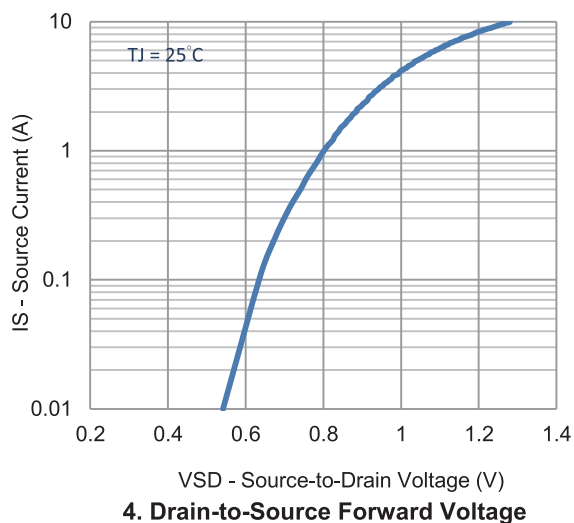
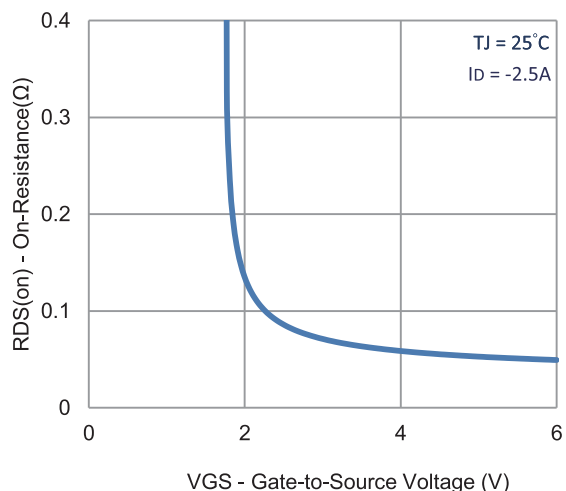
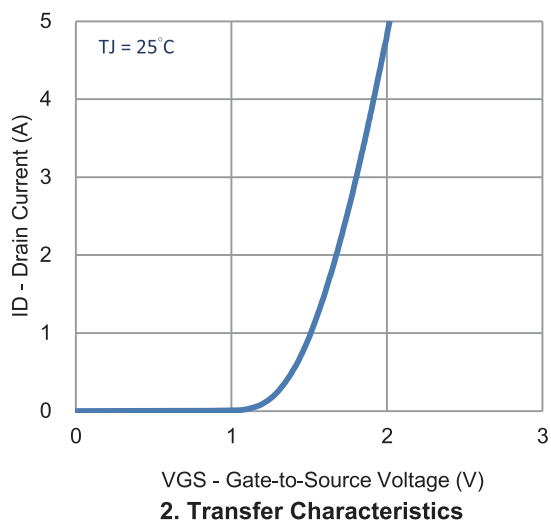
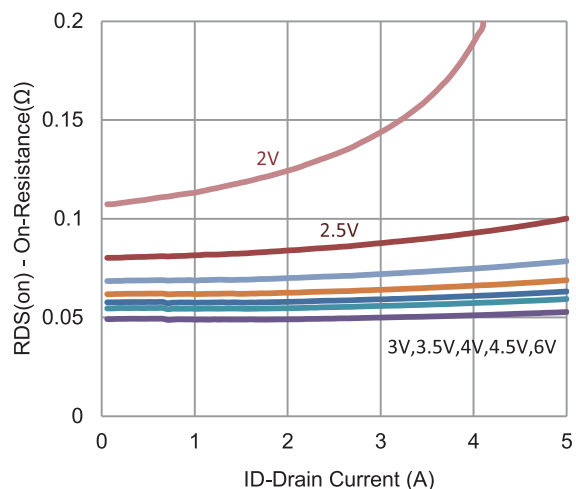
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24 V, V_{GS} = 0 V$			-1	μA
		$V_{DS} = -24 V, V_{GS} = 0 V, T_J = 55^\circ C$			-25	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5 V, V_{GS} = -4.5 V$	-5			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = -4.5 V, I_D = -2.5 A$			57	m Ω
		$V_{GS} = -2.5 V, I_D = -2 A$			89	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15 V, I_D = -2.5 A$		10		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -0.9 A, V_{GS} = 0 V$		-0.8		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = -15 V, V_{GS} = -4.5 V,$ $I_D = -2.5 A$		12		nC
Gate-Source Charge	Q_{gs}			1.9		
Gate-Drain Charge	Q_{gd}			3.6		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -15 V, R_L = 6 \Omega,$ $I_D = -2.5 A,$ $V_{GEN} = -4.5 V, R_{GEN} = 6 \Omega$		12		ns
Rise Time	t_r			9		
Turn-Off Delay Time	$t_{d(off)}$			42		
Fall Time	t_f			15		
Input Capacitance	C_{iss}	$V_{DS} = -15 V, V_{GS} = 0 V, f = 1 MHz$		684		pF
Output Capacitance	C_{oss}			75		
Reverse Transfer Capacitance	C_{rss}			60		

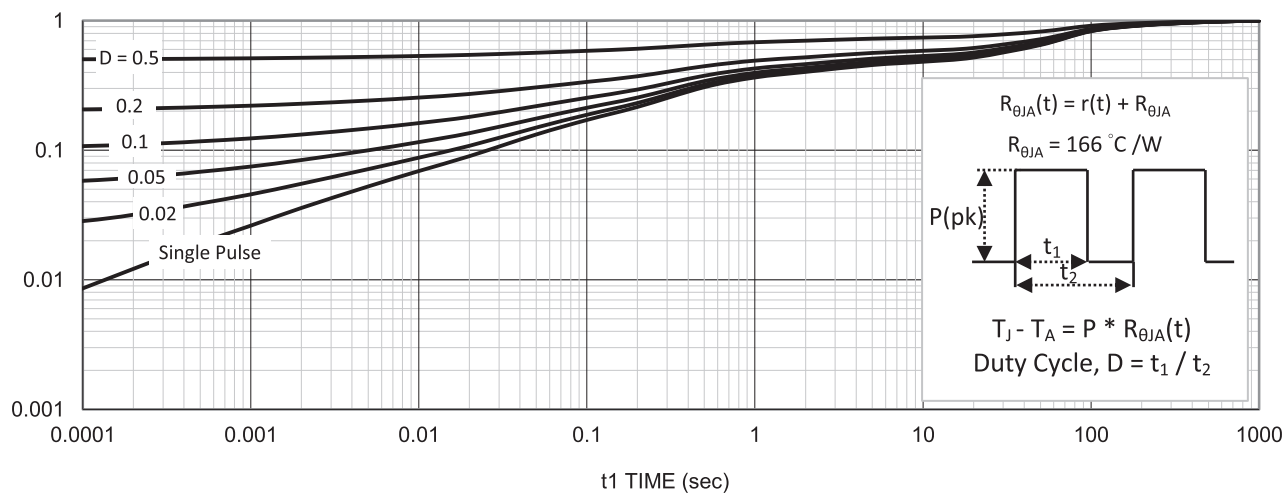
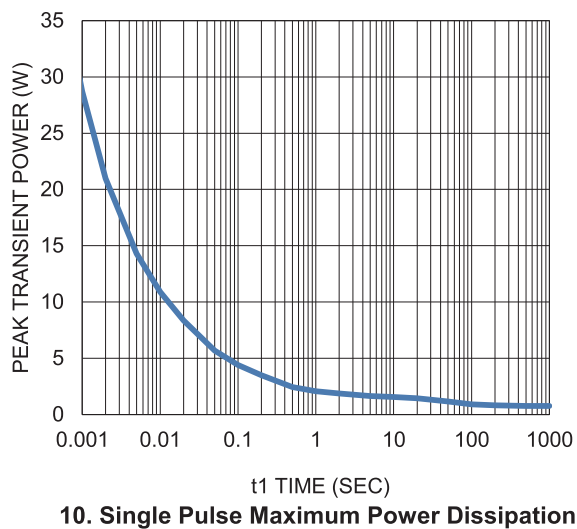
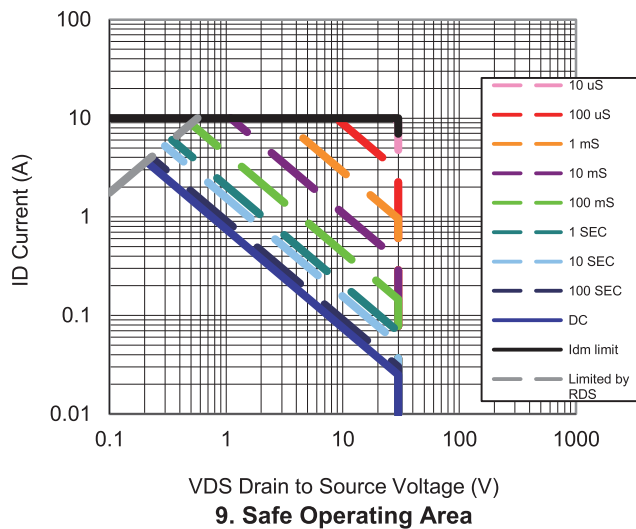
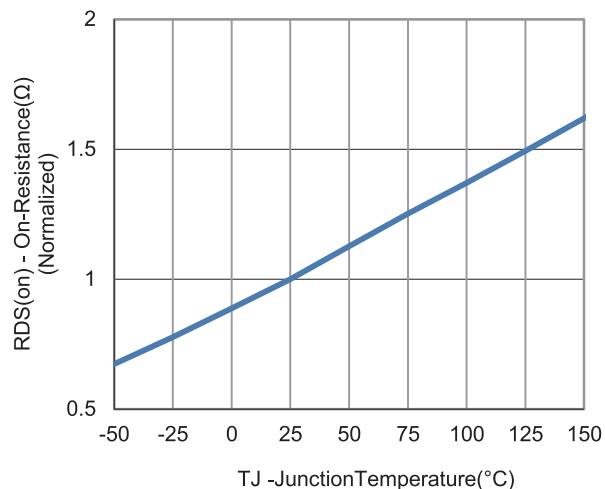
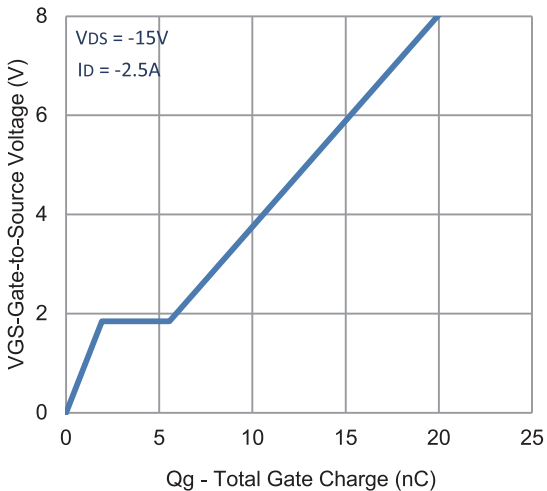
Notes

- a. Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

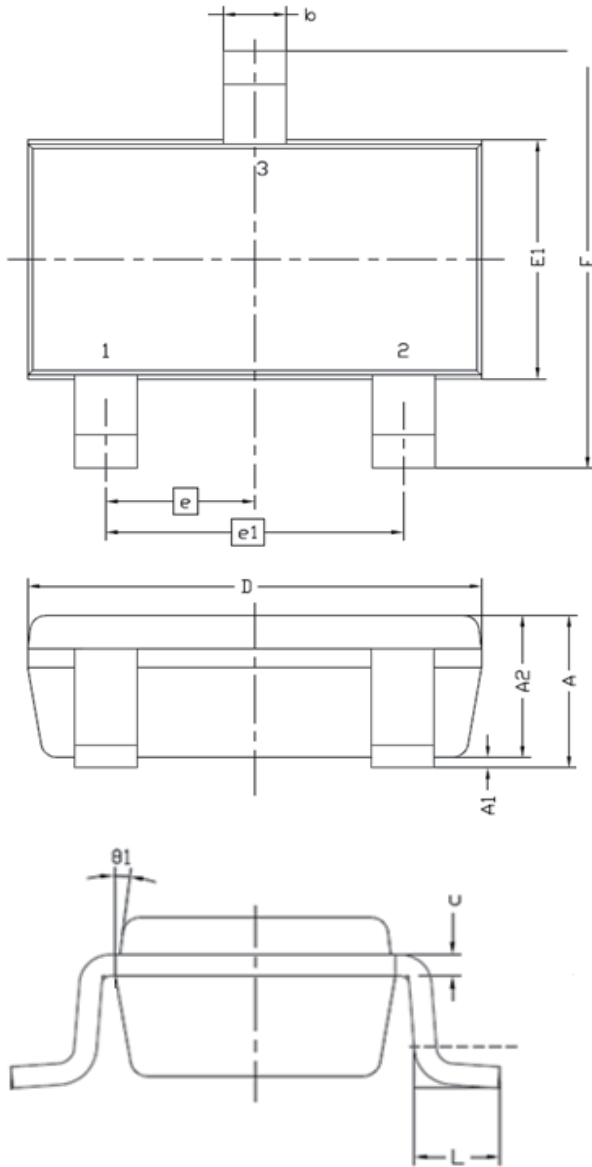
Typical Electrical Characteristics



Typical Electrical Characteristics

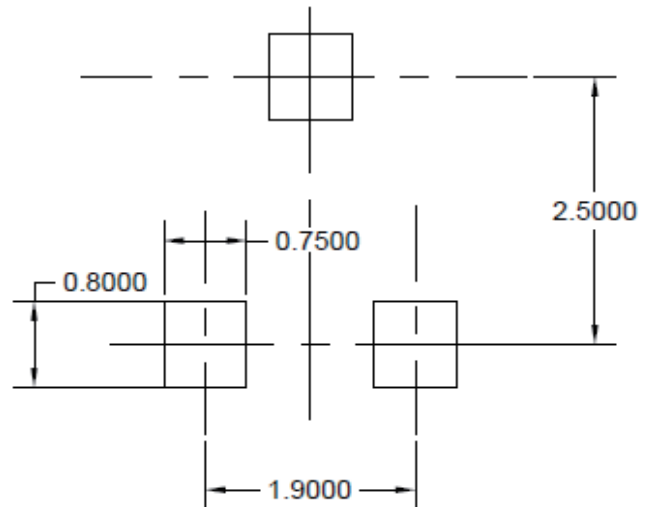


Package Information



Symbol	MILLIMETERS	
	MIN	MAX
A	0.8	1.2
A1	0	0.1
A2	0.7	1.1
b	0.3	0.5
c	0.1	0.2
D	2.7	3.1
E	2.6	3
E1	1.4	1.8
e	0.95 BSC	
e1	1.9 BSC	
L	0.3	0.6
θ1	7° NOM	

Recommended Pad Layout



Note: Drain opening is recommended to be solder mask defined in a copper fill for improved thermal performance

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