

150V_{DS}/±20V_{GS} N-Channel Enhancement Mode MOSFET

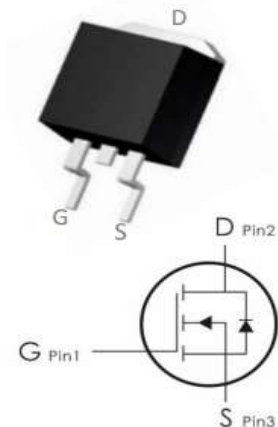
Features

- V_{DS}=150V, I_D=100A
- R_{DS(ON)}=7mΩ (TYP.) V_{GS}=10V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability

Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

TO-263



Ordering Information

Device	package	Device Marking	Package Qty.
LW4115A5	TO-263	LW4115A5	800/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	150	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	100	A
Continuous Drain Current (T _C =100°C)		70	A
Pulsed Drain Current	I _{DM}	440	A
Maximum Power Dissipation	P _D	192	W
Single pulse avalanche energy	E _{as}	625	mJ
Operating, Storage Temperature Range	T _J , T _{STG}	-55~175	°C

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1		4	V
Drain-Source On-state Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	-	7	mΩ

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	-	0.65	-	$^{\circ}\text{C}/\text{W}$
Thermal Resistance,Junction-to-Ambient	$R_{\theta JA}$		50		$^{\circ}\text{C}/\text{W}$

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15\text{V}$	-	5928	-	pF
Output capacitance	C_{oss}	$V_{GS}=0\text{V}$	-	545	-	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$	-	22	-	
Gate Resistance	R_g	$f=1\text{MHz}$	-	1.5	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15\text{V}$	-	84.2	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=10\text{V}$	-	24.7	-	
Gate Drain Charge	Q_{gd}	$I_D=20\text{A}$	-	16.8	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}$	-	31	-	ns
Rise time	t_r	$V_{DS}=15\text{V}$	-	48	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=0.75\Omega$	-	79	-	
Fall time	t_f	$R_G=3\Omega$	-	45	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_{SD}=1\text{A}$	-	-	1.0	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0\text{V}, I_{SD}=20\text{A}$	-	94	-	ns
Reverse Recovery Charge	Q_{rr}	$d_i/d_t=500\text{A}/\mu\text{s}$	-	362	-	nC

Electrical Characteristics Diagrames

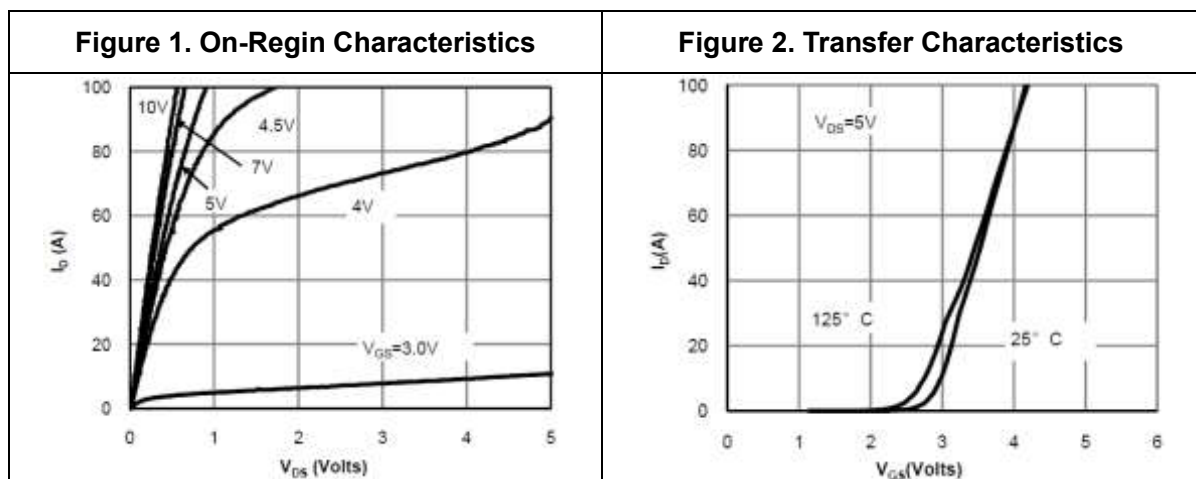


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

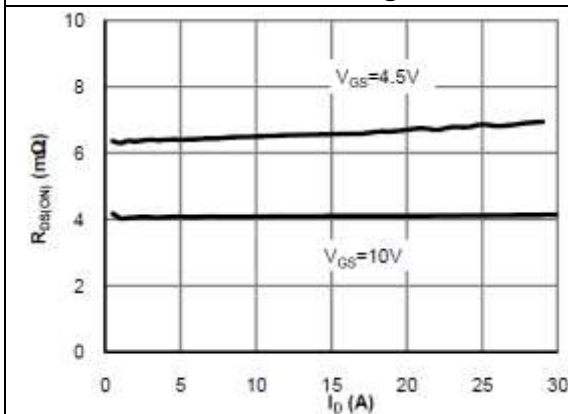


Figure 4. On-Resistance vs. Junction Temperature

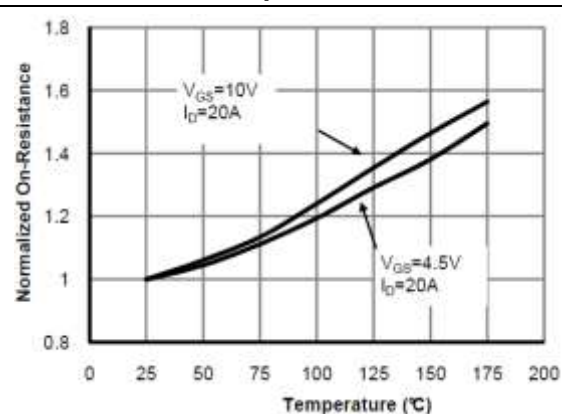


Figure 5. On-Resistance vs. Gate-Source Voltage

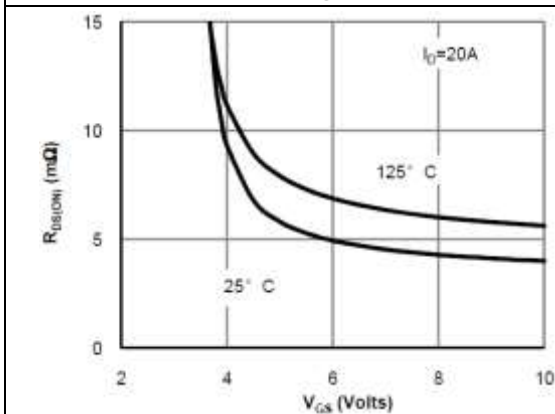


Figure 6. Body-Diode Characteristics

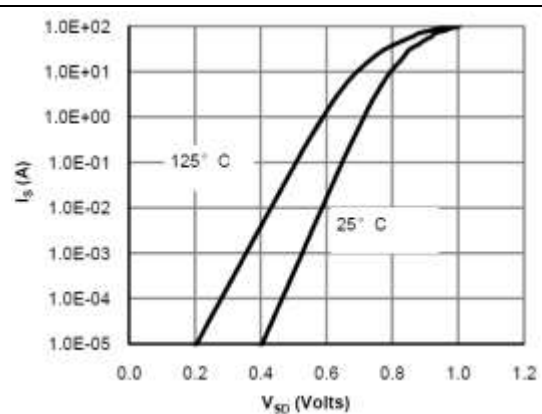


Figure 7. Gate-Charge Characteristics

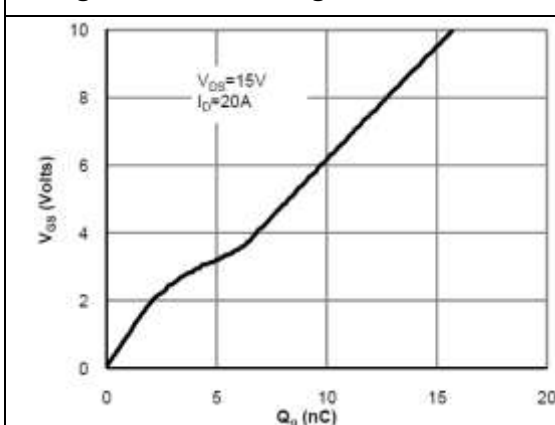


Figure 8. Capacitance Characteristics

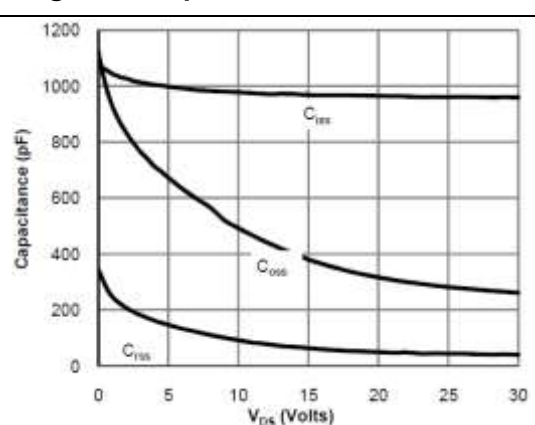


Figure 9. Maximum Forward Biased Safe Operating Area

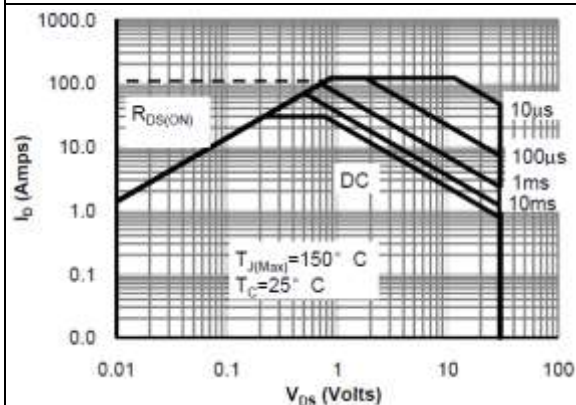


Figure 10. Single Pulse Power Rating Junction-to-Case

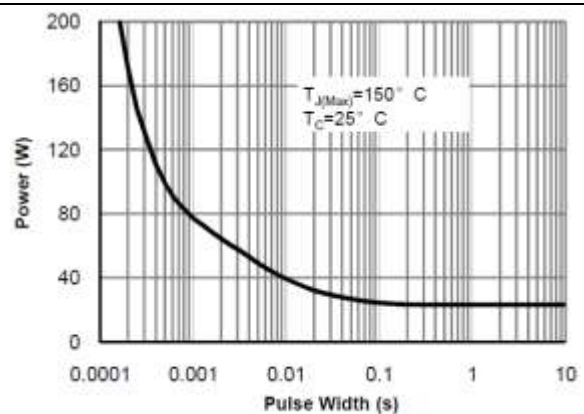
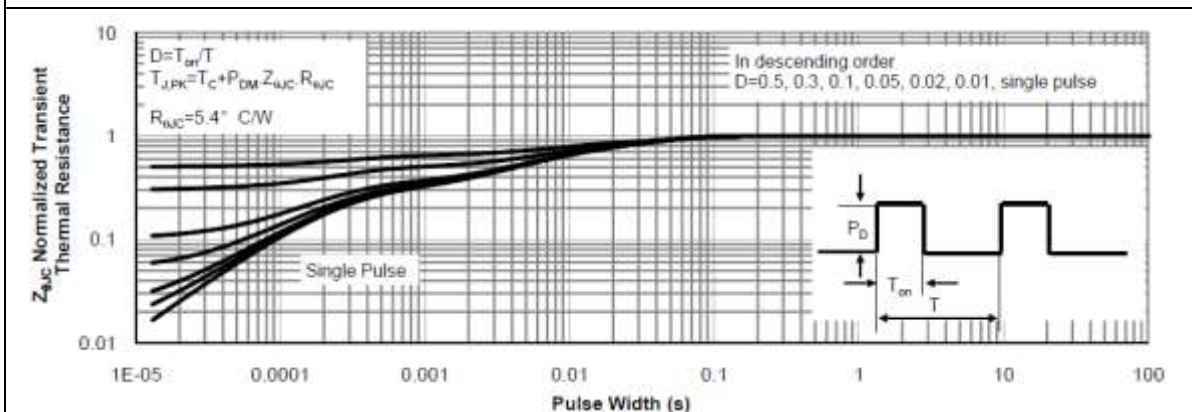


Figure 11. Normalized Maximum Transient Thermal Impedance



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