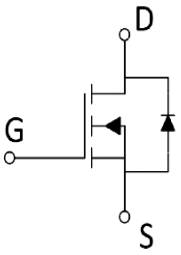
# LN4517T1G

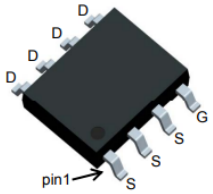
## 60$V_{DS}$/±20V N-Channel Enhancement Mode MOSFET

### Features

- $V_{DS}$=60V,$I_D$=10A
- $R_{DS(ON)}$=18mΩ（TYP.）$V_{GS}$=10V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance




SOP8




### Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

### Ordering Information

| Device | package | Device Marking | Package Qty. |
|---|---|---|---|
| **LN4517T1G** | **SOP-8** | ** | **3000/PCS** |

### Absolute Maximum Ratings ($T_C$=25℃,unless otherwise noted)

| Parameter | Symbol | Value | Unit |
|---|---|---|---|
| Drain-Source Voltage ($V_{GS}$=0V) | $V_{DS}$ | 60 | V |
| Gate-Source Voltage ($V_{GS}$=0V,static) | $V_{GS}$ | ±20 | V |
| Continuous Drain Current ($T_C$=25℃) | $I_D$ | 10 | A |
| Continuous Drain Current ($T_C$=100℃) | | 5.6 | A |
| Pulesd Drain Current | $I_{DM}$ | 32 | A |
| Single Pulsed Avalanche Energy | $E_{AS}$ | 69 | mJ |
| Maximum Power Dissipation | $P_D$ | 2.1 | W |
| Operating,Storage Temperature Range | $T_J$,$T_{STG}$ | -55~150 | ℃ |

### Electrical Characteristics

| Parameter | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| Drain-Source Breakdown Voltage | $BV_{DSS}$ | $V_{GS}$=0V,$I_D$=250μA | 60 | - | - | V |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$=30V, $V_{GS}$=0V | - | - | 1 | μA |
| Gate -Source Leakage Current | $I_{GSS}$ | $V_{GS}$=±20V, $V_{DS}$=0V | - | - | ±100 | nA |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$= $V_{GS}$,$I_D$=250μA | 1.2 | 1.8 | 2.5 | V |
| Drain-Source On-stage Resistance | $R_{DS(ON)}$ | $V_{GS}$=10V,$I_D$=1A | - | 15 | 19 | mΩ |
| | | $V_{GS}$=4.5V,$I_D$=2A | - | 19 | 25 | |
| Body Diode Forward Voltage | $V_{SD}$ | $V_{GS}$=0V,$I_{SD}$=1A | - | - | 1.2 | V |

### Thermal Characteristics

| Parameter | Symbol | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|
| Thermal Resistance,Junction-to-Case | $R_{\theta JC}$ | - | - | - | ℃/ W |
| Thermal Resistance,Junction-to-Ambient | $R_{\theta JA}$ | - | 6- | - | ℃/ W |

## Dynamic Characteristics

| Parameter | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| Input capacitance | $C_{iss}$ | $V_{DS}$=15V | - | 1888 | - | pF |
| Output capacitance | $C_{oss}$ | $V_{GS}$=0V | - | 112 | - | |
| Reverse transfer capacitance | $C_{rss}$ | f=1MHz | - | 91 | - | |
| Gate Resistance | $R_g$ | f=1MHz | - | - | - | Ω |
| Total Gate Charge | $Q_g$ | $V_{DS}$=15V | - | 39 | - | nC |
| Gate Source Charge | $Q_{gs}$ | $V_{GS}$=10V | - | 7.7 | - | |
| Gate Drain Charge | $Q_{gd}$ | $I_D$=20A | - | 8.3 | - | |
| Turn-on delay Time | $t_{d(on)}$ | $V_{GS}$=10V | - | 6.7 | - | ns |
| Rise time | $t_r$ | $V_{DS}$=15V | - | 39 | - | |
| Turn-off delay Time | $t_{d(off)}$ | $R_L$=3.5Ω | - | 21 | - | |
| Fall time | $t_f$ | $R_G$=6.8Ω | - | 6.2 | - | |
| Reverse Recovery Time | $t_{rr}$ | $V_{GS}$=0V,$I_{SD}$=20A | - | 29 | - | ns |
| Reverse Recovery Charge | $Q_{rr}$ | $d_i/d_t$=100A/μs | - | 21 | - | nC |

## Electrical Characteristics Diagrames

**Figure 1. On-Regin Characteristics**

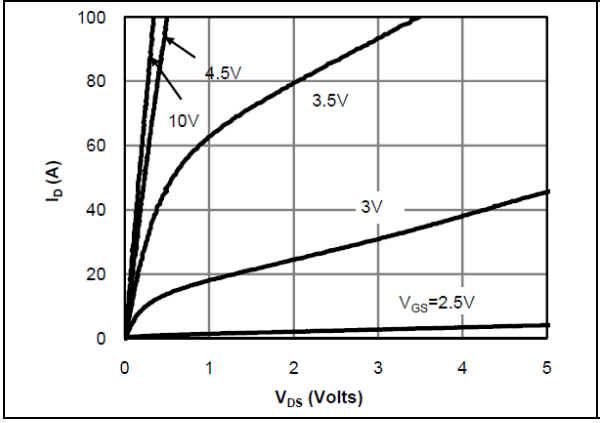



**Figure 2. Transfer Characteristics**

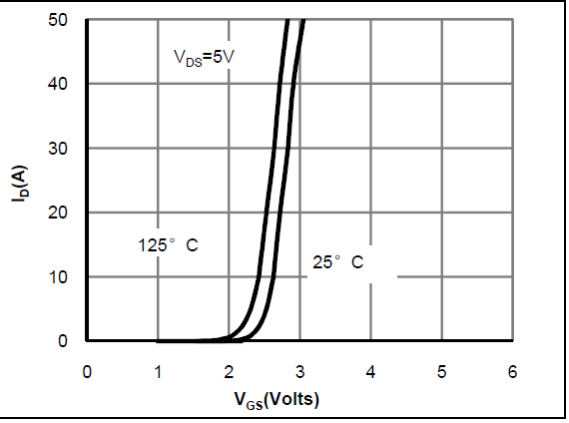



**Figure 3. On-Resistance vs. Drain Current and Gate Voltage**

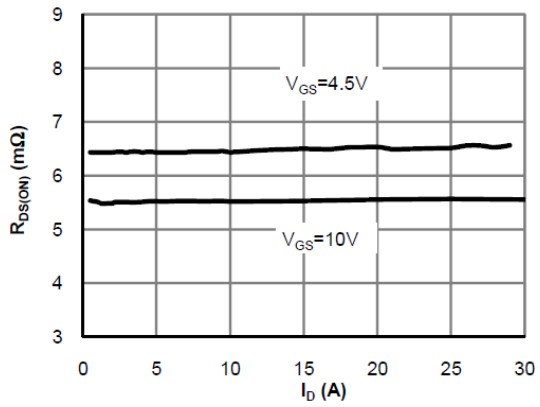



**Figure 4. On-Resistance vs. Junction Temperature**

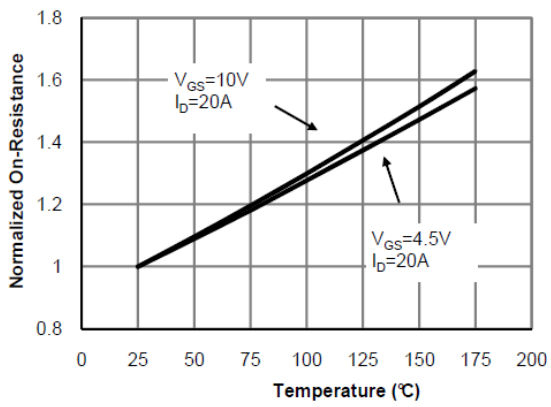


**Figure 5. On-Resistance vs. Gate-Source Voltage**

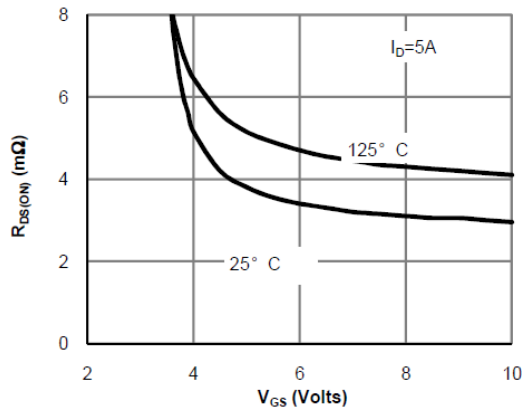



**Figure 6. Body-Diode Characteristics**

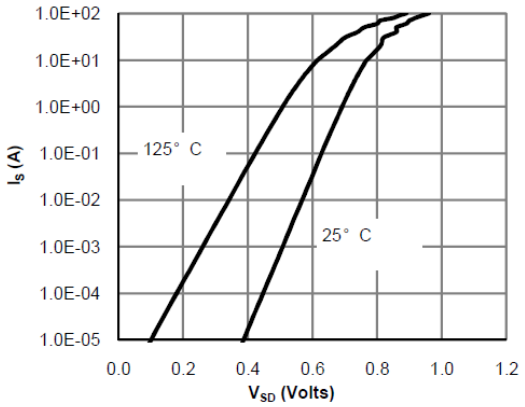



**Figure 7. Gate-Charge Characteristics**

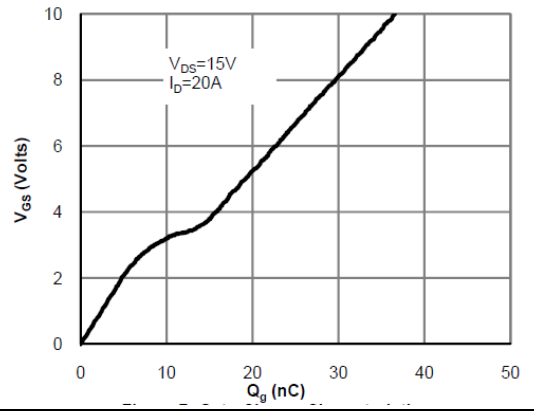



**Figure 8. Capacitance Characteristics**

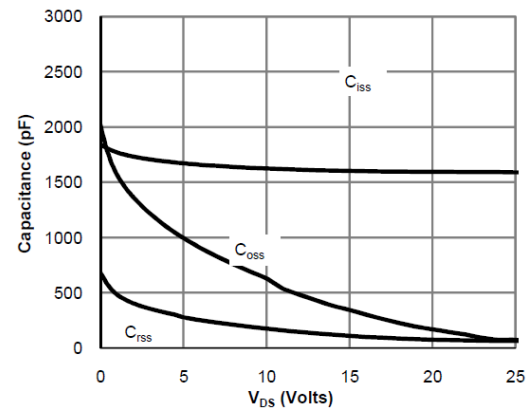



**Figure 9. Maximum Forward Biased Safe Operating Area**

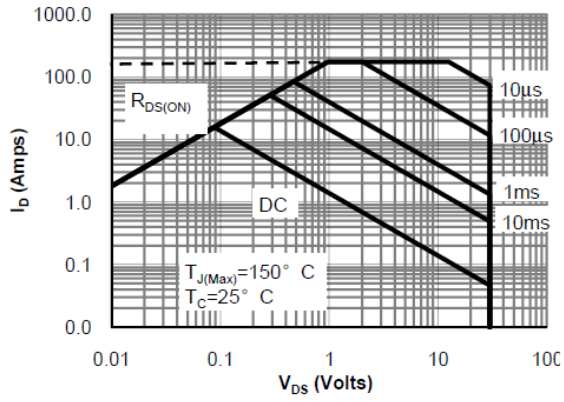



**Figure 10. Single Pulse Power Rating Junction-to-Ambient**