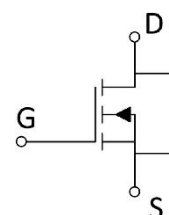
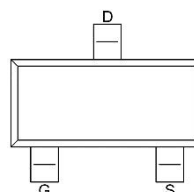
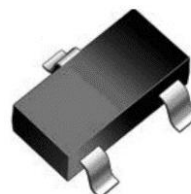


-30V_{DS}/±20V_{GS} P-Channel Enhancement Mode MOSFET

Features

- $V_{DS}=-30V, I_D=-4.2A$
- $R_{DS(ON)}=60m\Omega$ (TYP.) $V_{GS}=10V$
- $R_{DS(ON)}=87m\Omega$ (TYP.) $V_{GS}=4.5V$

SOT23-3L



Applications

- Portable device
- Switch switching

Ordering Information

Device	package	Device Marking	Package Qty.
JSTL3407	SOT-23-3L	R7/X7	3000/PCS
JSTL3407	SOT-23	R7/A7	3000/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	-30	V
Gate-Source Voltage (V _{GS} =0V,static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	-4.1	A
Pulse Drain Current	I _{DM}	20	A
Maximum Power Dissipation (T _C =25°C)	P _D	1.25	W
Operating,Storage Temperature Range	T _J ,T _{STG}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance,Junction-to-Case	R _{θJC}	-	60	-	°C/W
Thermal Resistance,Junction-to-Ambient	R _{θJA}	-	100	-	°C/W

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
Gate -Source Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.4	3	V
Drain-Source On-stage Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=5.8A$	-	46	65	m Ω
		$V_{GS}=4.5V, I_D=5A$	-	68	90	

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$ $V_{GS}=0V$ $f=1MHz$	-	700	-	pF
Output capacitance	C_{oss}		-	120	-	
Reverse transfer capacitance	C_{rss}		-	75	-	
Gate Resistance	R_g	$f=1MHz$	-	1.4	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$ $V_{GS}=4.5V$ $I_D=5.8A$	-	7.2	-	nC
Gate Source Charge	Q_{gs}		-	1.5	-	
Gate Drain Charge	Q_{gd}		-	2.6	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$ $V_{DS}=15V$ $R_L=2.7\Omega$ $R_G=3\Omega$	-	8.6	-	ns
Rise time	t_r		-	5.0	-	
Turn-off delay Time	$t_{d(off)}$		-	28.2	-	
Fall time	t_f		-	13.5	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-	-	1.0	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=5A$ $d_i/d_t=100A/\mu s$	-	16	-	ns
Reverse Recovery Charge	Q_{rr}		-	9	-	nC

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