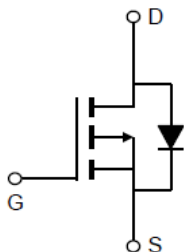


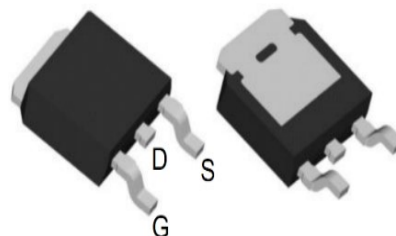
-40V_{DS}/±20V_{GS} P-Channel Enhancement Mode MOSFET

Features

- $V_{DS}=-40V, I_D=-23A$
- $R_{DS(ON)}=40m\Omega$ (TYP.) $V_{GS}=-10V$
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability



TO-252



Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

Ordering Information

Device	package	Device Marking	Package Qty.
JMTK440P04A	TO-252	**	2500/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	-40	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	-23	A
Continuous Drain Current (T _C =100°C)		-15	A
Pulsed Drain Current	I _{DM}	-88	A
Avalanche Energy, Single Pulsed	E _{AS}	41	mJ
Maximum Power Dissipation (T _C =25°C)	P _D	35	W
Operating, Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	3.6	-	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	-	60	-	°C/W

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-	-1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±10	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =-250μA	-1.0	-1.5	-2.5	V
Drain-Source On-stage Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-1A	-	30	40	mΩ
		V _{GS} =-4.5V, I _D =-1A	-	35	50	

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=-15V$	-	1033	-	pF
Output capacitance	C_{oss}	$V_{GS}=0V$	-	106	-	
Reverse transfer capacitance	C_{rss}	$f=1MHz$	-	80	-	
Gate Resistance	R_g	$f=1MHz$	-	-	-	Ω
Total Gate Charge	Q_g	$V_{DS}=-15V$	-	19	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=-10V$	-	3	-	
Gate Drain Charge	Q_{gd}	$I_D=-15A$	-	2	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=-10V$	-	9	-	ns
Rise time	t_r	$V_{DS}=-15V$	-	21	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=1\Omega$	-	26	-	
Fall time	t_f	$R_G=3\Omega$	-	32	-	
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=-1A$	-	-	-1.2	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=-15A$	-	14	-	ns
Reverse Recovery Charge	Q_{rr}	$dI/dt=100A/\mu s$	-	9	-	nC

Electrical Characteristics Diagrams

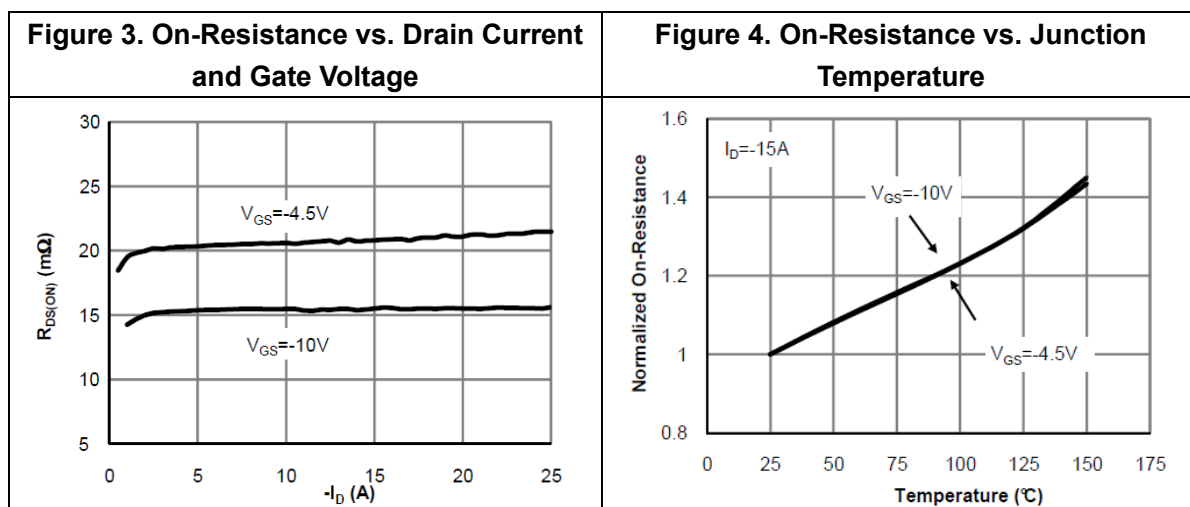
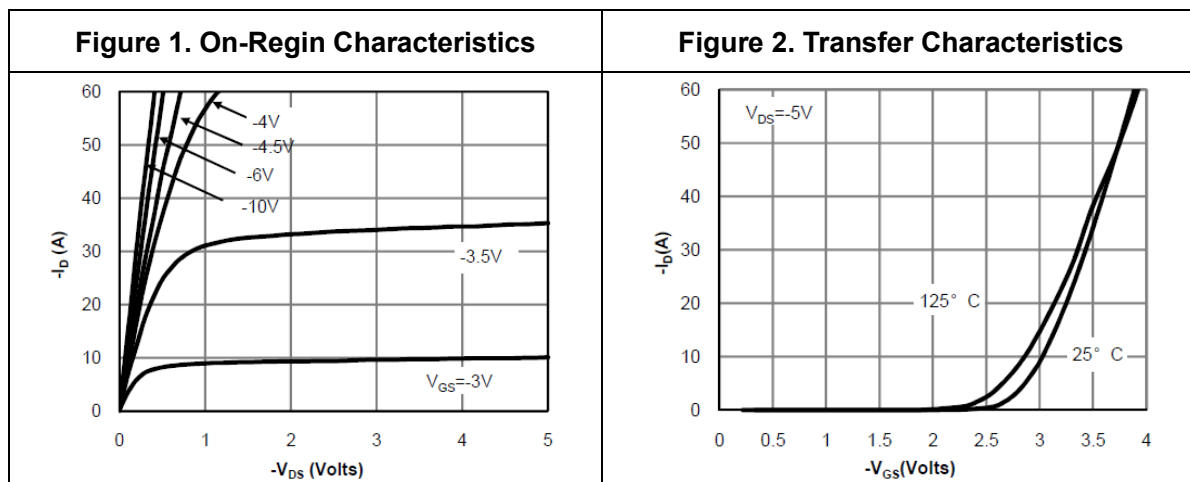
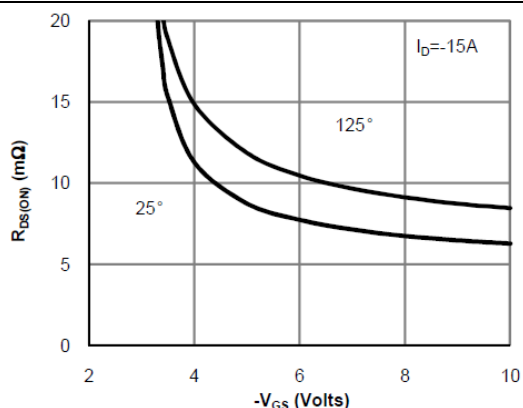
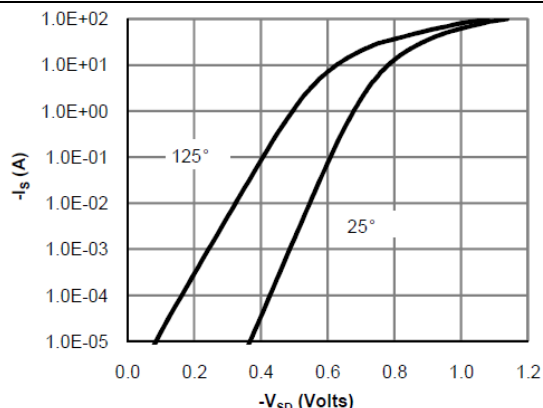
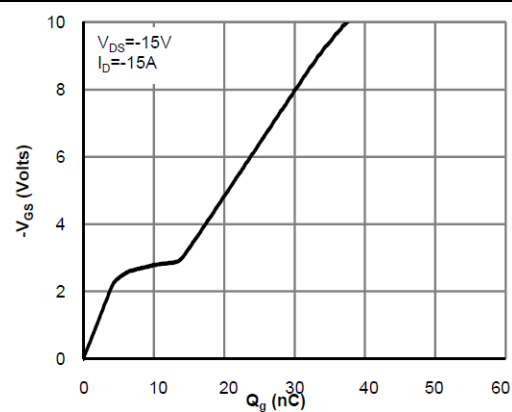
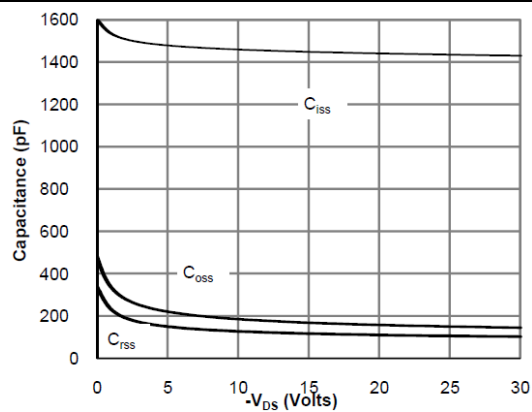
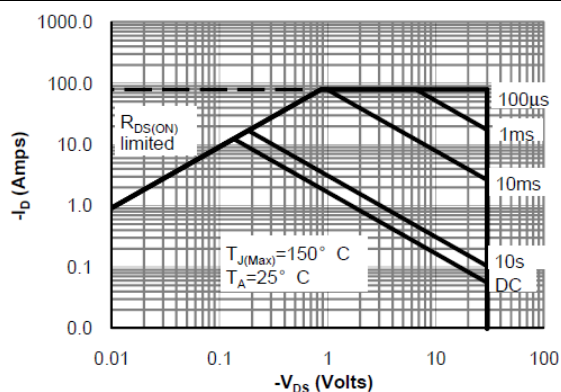
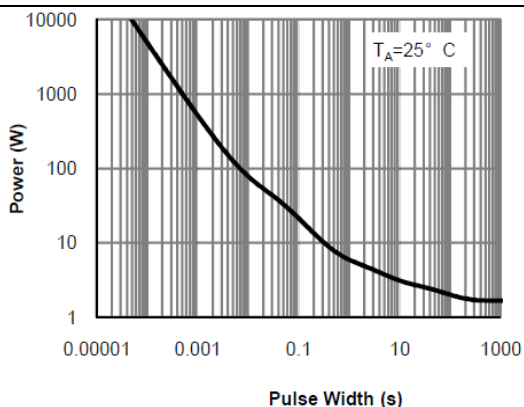


Figure 5. On-Resistance vs. Gate-Source Voltage

Figure 6. Body-Diode Characteristics

Figure 7. Gate-Charge Characteristics

Figure 8. Capacitance Characteristics

Figure 9. Maximum Forward Biased Safe Operating Area

Figure 10. Single Pulse Power Rating Junction-to-Ambient


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