

40V_{DS}/±20V_{GS} N-Channel Enhancement Mode MOSFET
Features

- $V_{DS}=40V, I_D=10A$
- $R_{DS(ON)}=13m\Omega$ (TYP.) $V_{GS}=10V$
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance

Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

Ordering Information

Device	package	Device Marking	Package Qty.
HMN4984	SOP-8	M4984	3000/PCS

Absolute Maximum Ratings ($T_C=25^\circ C$, unless otherwise noted)

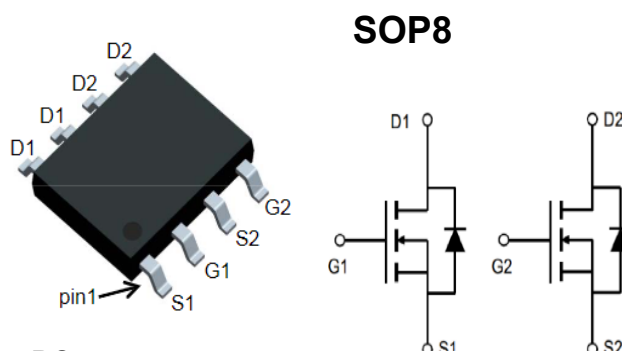
Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	40	V
Gate-Source Voltage ($V_{GS}=0V$, static)	V_{GS}	±20	V
Continuous Drain Current ($T_C=25^\circ C$)	I_D	10	A
Continuous Drain Current ($T_C=100^\circ C$)		7.3	A
Pulsed Drain Current	I_{DM}	50	A
Single Pulsed Avalanche Energy	E_{AS}	66	mJ
Maximum Power Dissipation ($T_C=25^\circ C$)	P_D	2	W
Operating, Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	6.8	-	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	-	60	-	$^\circ C/W$

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
Gate -Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.5	2	V
Drain-Source On-stage Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=1A$	-	10	13	m Ω
		$V_{GS}=4.5V, I_D=1A$	-	15	20	



Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$ $V_{GS}=0V$ $f=1MHz$	-	965	-	pF
Output capacitance	C_{oss}		-	110	-	
Reverse transfer capacitance	C_{rss}		-	95	-	
Gate Resistance	R_g	$f=1MHz$	-	-	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$ $V_{GS}=10V$ $I_D=20A$	-	23	-	nC
Gate Source Charge	Q_{gs}		-	3.5	-	
Gate Drain Charge	Q_{gd}		-	5.5	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$ $V_{DS}=15V$ $R_L=3.5\Omega$ $R_G=6.8\Omega$	-	5.5	-	ns
Rise time	t_r		-	15	-	
Turn-off delay Time	$t_{d(off)}$		-	25	-	
Fall time	t_f		-	12	-	
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-	-	1.2	V

Electrical Characteristics Diagrames

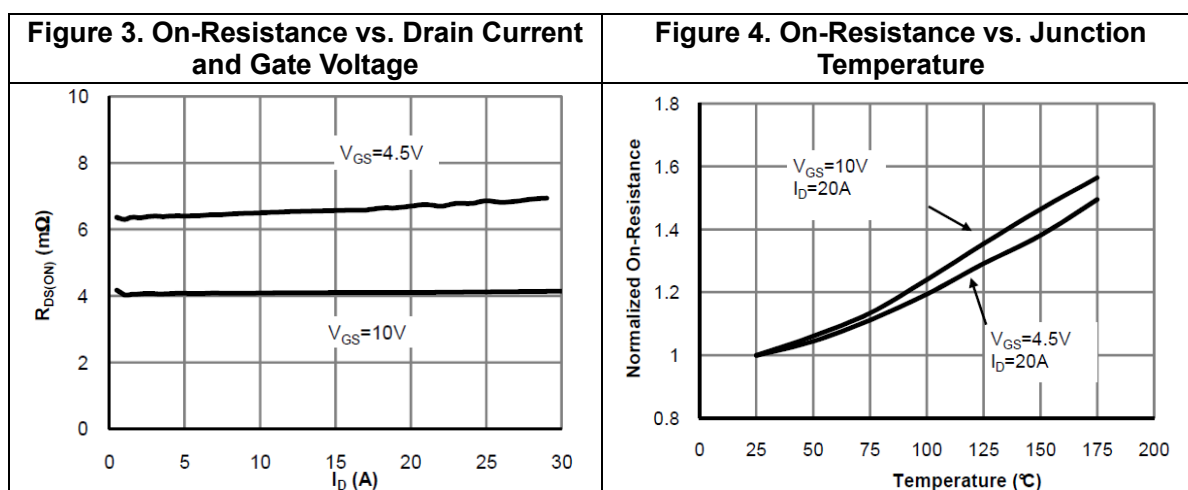
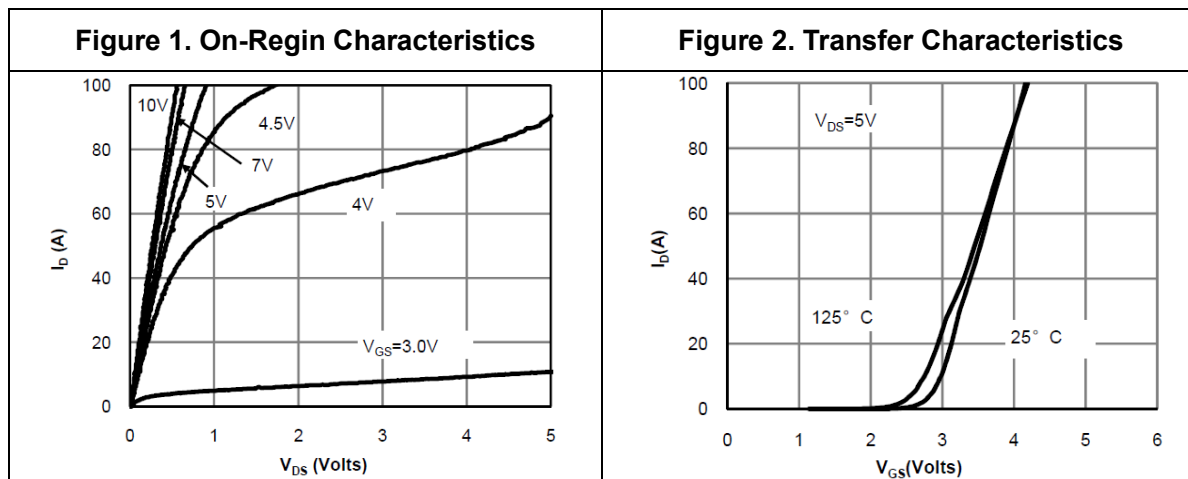
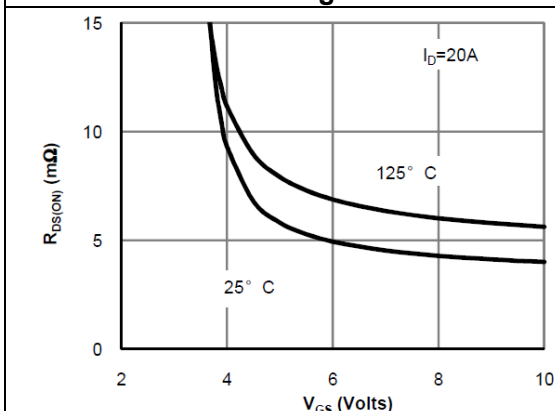
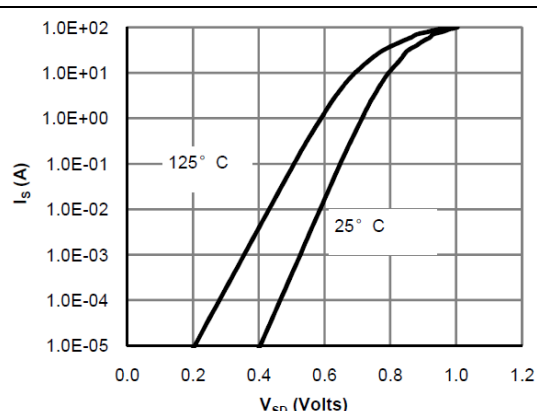
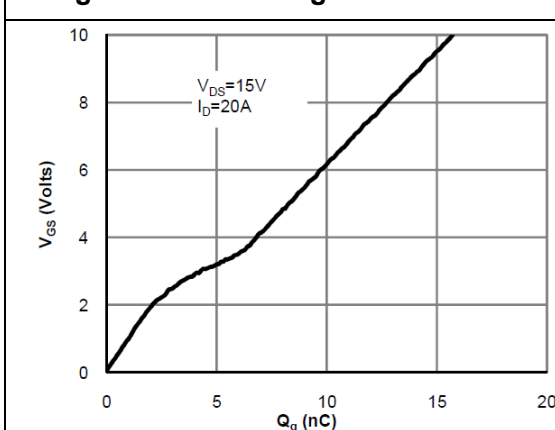
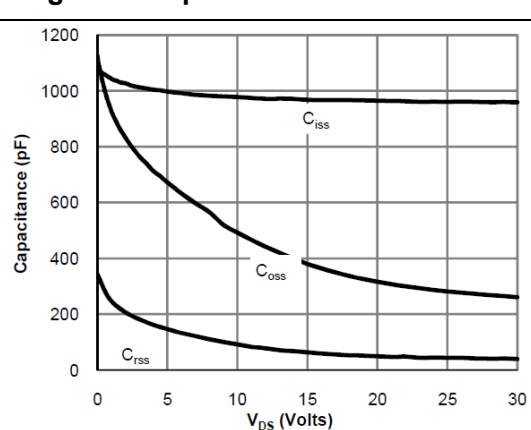
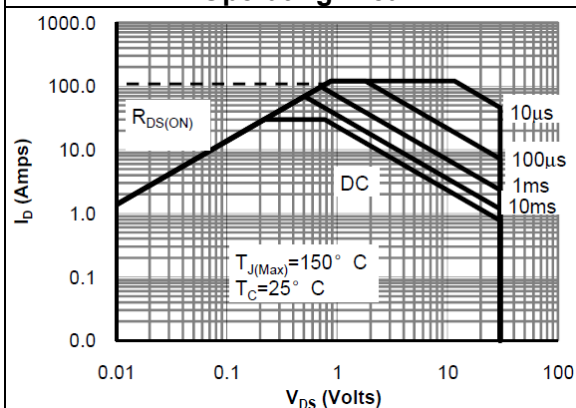
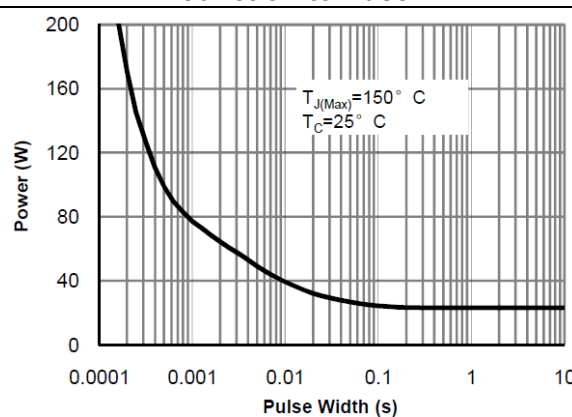


Figure 5. On-Resistance vs. Gate-Source Voltage

Figure 6. Body-Diode Characteristics

Figure 7. Gate-Charge Characteristics

Figure 8. Capacitance Characteristics

Figure 9. Maximum Forward Biased Safe Operating Area

Figure 10. Single Pulse Power Rating Junction-to-Case


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