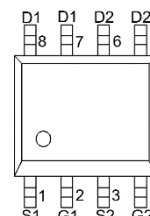
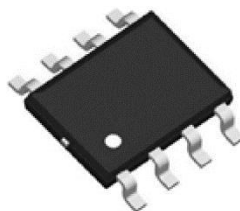


40V_{DS}/±20V_{GS} N-Channel Enhancement Mode MOSFET

Features

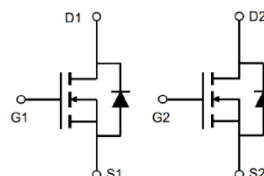
- V_{DS}=40V, I_D=8A
- R_{DS(ON)}=21mΩ (TYP.) V_{GS}=10V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance

SOP8



Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion



Ordering Information

Device	package	Device Marking	Package Qty.
HMN4882	SOP-8	N4882	3000/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	40	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	8	A
Continuous Drain Current (T _C =100°C)		6	A
Pulsed Drain Current	I _{DM}	40	A
Single Pulsed Avalanche Energy	E _{AS}	13	mJ
Maximum Power Dissipation (T _C =25°C)	P _D	2.9	W
Operating, Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	-	-	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	-	60	-	°C/W

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
Gate -Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.5	V
Drain-Source On-stage Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=1A$	-	17	23	m Ω
		$V_{GS}=4.5V, I_D=1A$	-	25	35	

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$	-	633	-	pF
Output capacitance	C_{oss}	$V_{GS}=0V$	-	67	-	
Reverse transfer capacitance	C_{rss}	$f=1MHz$	-	58	-	
Gate Resistance	R_g	$f=1MHz$	-	-	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$	-	12	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=10V$	-	3.2	-	
Gate Drain Charge	Q_{gd}	$I_D=20A$	-	3.1	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$	-	4	-	ns
Rise time	t_r	$V_{DS}=15V$	-	3	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=3.5\Omega$	-	15	-	
Fall time	t_f	$R_G=6.8\Omega$	-	2	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-	-	1.2	V

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