

-150V_{DS}/±20V_{GS} P-Channel Enhancement Mode MOSFET

Features

- $V_{DS}=-150V, I_D=-5A$
- $R_{DS(ON)}=720m\Omega$ (TYP.) $V_{GS}=-10V$
- Fast Switching
- Low On-Resistance

Applications

- Switch switching
- Power management in portable/desktop PCs

Ordering Information

Device	package	Device Marking	Package Qty.
HM15P05A4	TO-252	M15P05A4	2500/PCS

Absolute Maximum Ratings ($T_C=25^{\circ}C$, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	-150	V
Gate-Source Voltage ($V_{GS}=0V$, static)	V_{GS}	±20	V
Continuous Drain Current ($T_C=25^{\circ}C$)	I_D	-5	A
Continuous Drain Current ($T_C=100^{\circ}C$)		-3	A
Pulse Drain Current	I_{DM}	-40	A
Avalanche Energy, Single Pulsed	E_{AS}	230	mJ
Maximum Power Dissipation ($T_C=25^{\circ}C$)	P_D	80	W
Operating, Storage Temperature Range	T_J, T_{STG}	-55~150	$^{\circ}C$

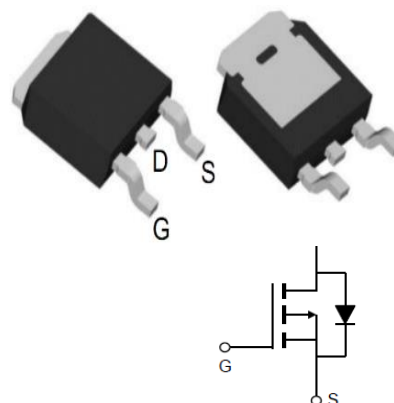
Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	2.8	-	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	-	62	-	$^{\circ}C/W$

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-150	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	μA
Gate -Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±10	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-2	-3	-4	V
Drain-Source On-stage Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-1A$	-	620	975	m Ω
		$V_{GS}=-4.5V, I_D=-1A$	-	-	-	

TO-252



Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=-15V$ $V_{GS}=0V$ $f=1MHz$	-	1900	-	pF
Output capacitance	C_{oss}		-	240	-	
Reverse transfer capacitance	C_{rss}		-	137	-	
Gate Resistance	R_g	$f=1MHz$	-	6	-	Ω
Total Gate Charge	Q_g	$V_{DS}=-15V$ $V_{GS}=-10V$ $I_D=-15A$	-	36	-	nC
Gate Source Charge	Q_{gs}		-	3	-	
Gate Drain Charge	Q_{gd}		-	-	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=-10V$ $V_{DS}=-15V$ $R_L=1\Omega$ $R_G=3\Omega$	-	16	-	ns
Rise time	t_r		-	19	-	
Turn-off delay Time	$t_{d(off)}$		-	70	-	
Fall time	t_f		-	41	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=-1A$	-	-0.75	-1.2	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=-15A$ $dI/dt=100A/\mu s$	-	90	-	ns
Reverse Recovery Charge	Q_{rr}		-	70	-	nC

Electrical Characteristics Diagrames

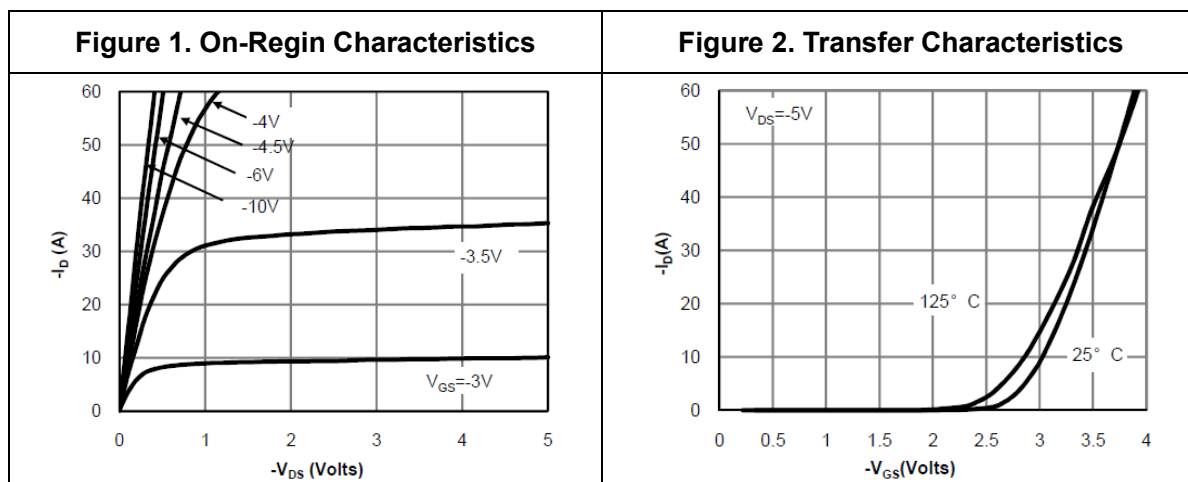


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

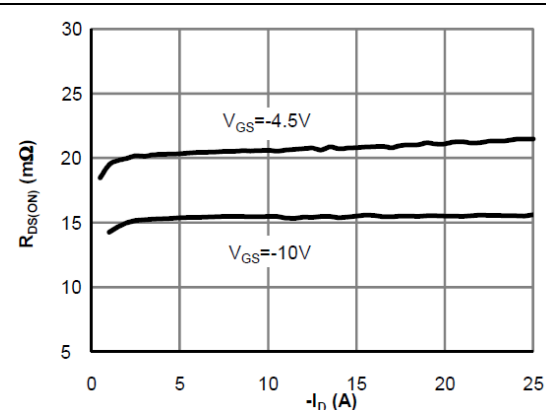


Figure 4. On-Resistance vs. Junction Temperature

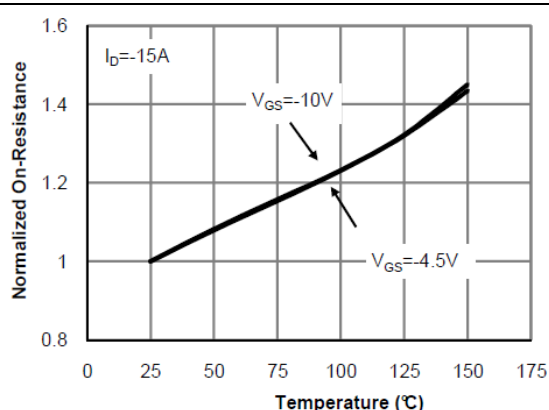


Figure 5. On-Resistance vs. Gate-Source Voltage

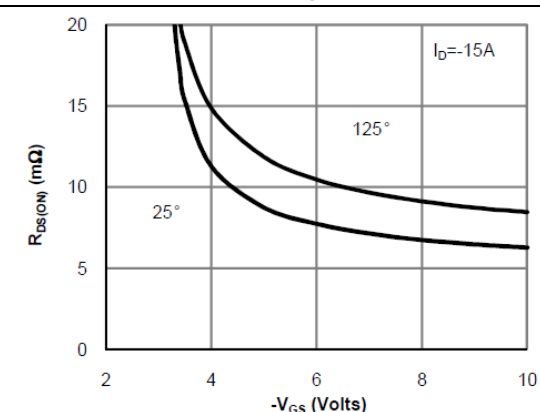


Figure 6. Body-Diode Characteristics

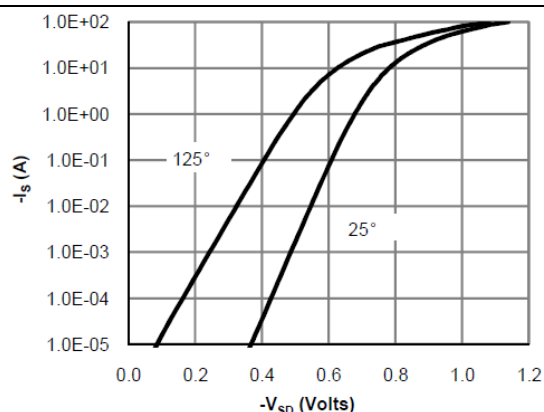


Figure 7. Gate-Charge Characteristics

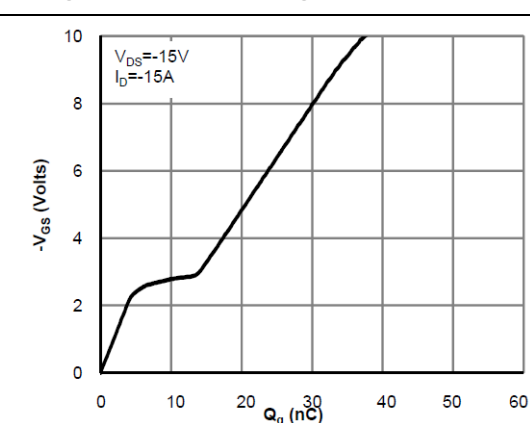


Figure 8. Capacitance Characteristics

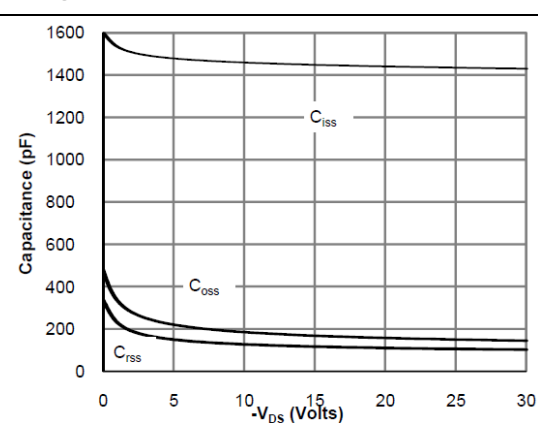


Figure 9. Maximum Forward Biased Safe Operating Area

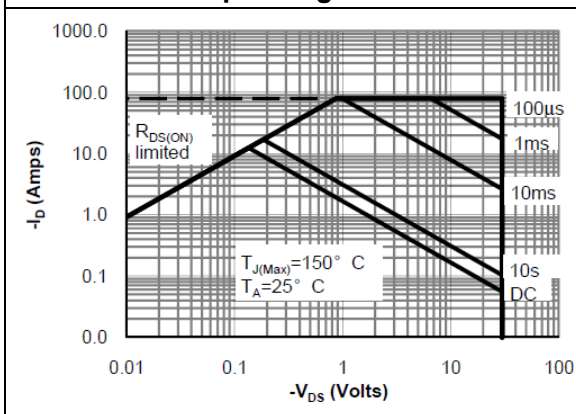


Figure 10. Single Pulse Power Rating Junction-to-Ambient

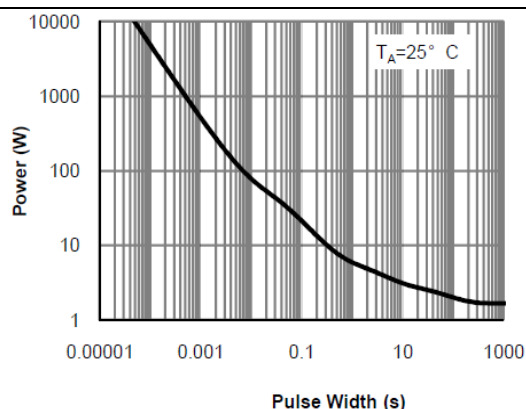
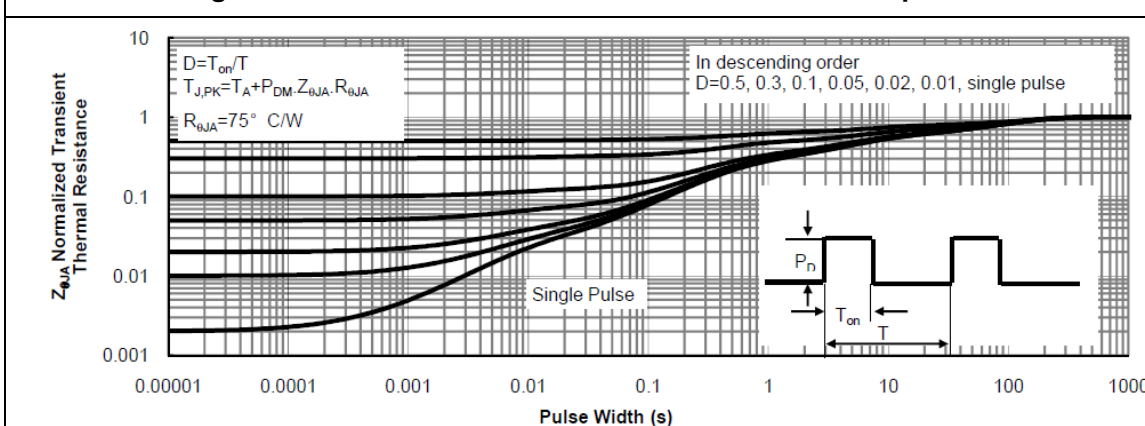


Figure 11. Normalized Maximum Transient Thermal Impedance



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