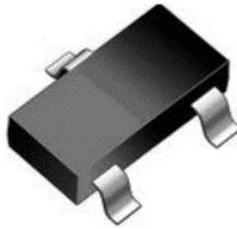
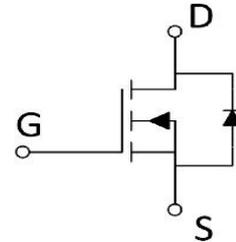
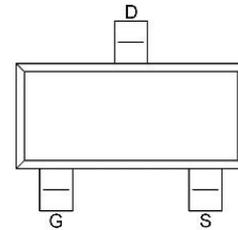


**-20V<sub>DS</sub> P-Channel Enhancement Mode MOSFET**
**Features**

- $V_{DS}=-20V, I_D=-2.9A$
- $R_{DS(ON)}=57m\Omega$  (TYP.)  $V_{GS}=4.5V$
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance


**SOT-23**

**Applications**

- Portable device
- Switch switching
- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion


**Ordering Information**

Device	Package		Marking	Package Qty.
AM2321PE-PF	SOT-23	Pb-Free	S21	3000pcs/Reel

**Absolute Maximum Ratings** ( $T_C=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ( $V_{GS}=0V$ )	$V_{DS}$	-20	V
Gate-Source Voltage ( $V_{GS}=0V$ , static)	$V_{GS}$	$\pm 12$	V
Continuous Drain Current ( $T_C=25^\circ C$ )	$I_D$	-2.9	A
Pulesd Drain Current	$I_{DM}$	12	A
Maximum Power Dissipation	$P_D$	0.35	W
Operating, Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ C$

**Thermal Characteristics**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	60	-	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	-	375	-	$^\circ C/W$

## Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	-20	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=24V, V_{GS}=0V$	-	-	1	$\mu A$
Gate -Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.4	-	0.9	V
Drain-Source Resistance	On-stage $R_{DS(ON)}$	$V_{GS}=4.5V, I_D=5.8A$	-	35	60	m $\Omega$
		$V_{GS}=2.5V, I_D=5A$	-	48	80	

## Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	$C_{iss}$	$V_{DS}=15V$	-	715	-	pF
Output capacitance	$C_{oss}$	$V_{GS}=0V$	-	170	-	
Reverse transfer capacitance	$C_{rss}$	$f=1MHz$	-	120	-	
Gate Resistance	$R_g$	$f=1MHz$	-	6	-	$\Omega$
Total Gate Charge	$Q_g$	$V_{DS}=15V$	-	-	13	nC
Gate Source Charge	$Q_{gs}$	$V_{GS}=4.5V$	-	1.2	-	
Gate Drain Charge	$Q_{gd}$	$I_D=5.8A$	-	2.2	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$	-	-	13	ns
Rise time	$t_r$	$V_{DS}=15V$	-	-	55	
Turn-off delay Time	$t_{d(off)}$	$R_L=2.7\Omega$	-	-	90	
Fall time	$t_f$	$R_G=3\Omega$	-	-	60	

## Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_{SD}=1A$	-	-	1.2	V
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0V, I_{SD}=5A$	-	16	-	ns
Reverse Recovery Charge	$Q_{rr}$	$d_i/d_t=100A/\mu s$	-	9	-	nC

< Copyright >

All the Patent, Copyright and IP contained in this document belong to HAMOS, shall not be reproduced, copied, or used in other ways without permission.