

Features

- V_{DS}=100V, I_D=75A
- R_{DS(ON)}=7.5mΩ (TYP.) V_{GS}=10V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability

Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

Ordering Information

Device	package	Device Marking	Package Qty.
GPT076N10LNC	PDFN5*6	**	5000/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	100	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	75	A
Continuous Drain Current (T _C =100°C)		49	A
Pulsed Drain Current	I _{DM}	300	A
Single Pulsed Avalanche Energy	E _{AS}	90	mJ
V _{DS} Spike 100ns	V _{SPIKE}	105	V
Maximum Power Dissipation (T _C =25°C)	P _D	97	W
Operating, Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1.0	-	2.5	V
Drain-Source On-stage Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	7.5	10	mΩ

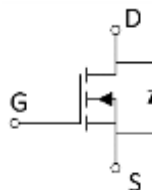
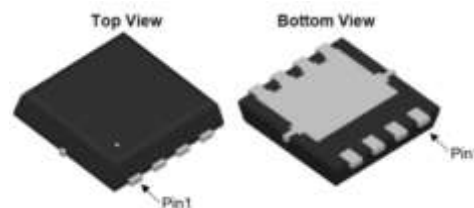
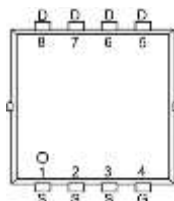
Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	2.3	-	°C/ W

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _{SD} =1A	-	0.7	1	V
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _{SD} =20A	-	10.4	-	ns
Reverse Recovery Charge	Q _{rr}	d _i /d _t =500A/μs	-	13.5	-	nC

PDFN5*6



Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$	-	2046	-	pF
Output capacitance	C_{oss}	$V_{GS}=0V$	-	865	-	
Reverse transfer capacitance	C_{rss}	$f=1MHz$	-	25	-	
Gate Resistance	R_g	$f=1MHz$	-	1.5	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$	-	15.9	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=10V$	-	2.9	-	
Gate Drain Charge	Q_{gd}	$I_D=20A$	-	3.3	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$	-	6.3	-	ns
Rise time	t_r	$V_{DS}=15V$	-	2.7	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=0.75\Omega$	-	18.6	-	
Fall time	t_f	$R_G=3\Omega$	-	4.2	-	

Electrical Characteristics Diagrams

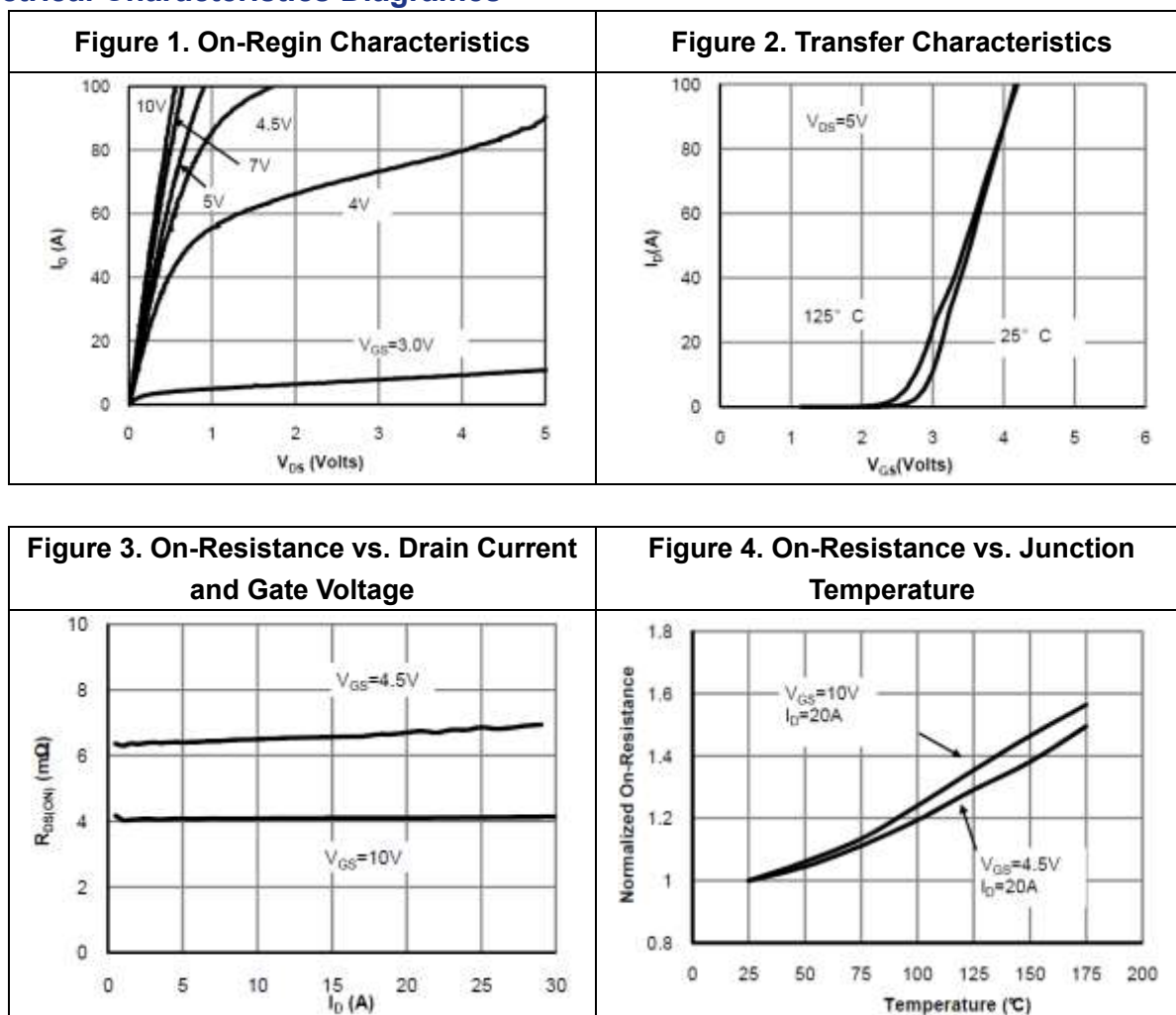


Figure 5. On-Resistance vs. Gate-Source Voltage

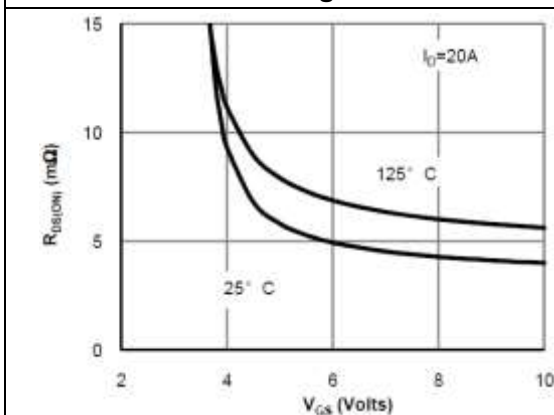


Figure 6. Body-Diode Characteristics

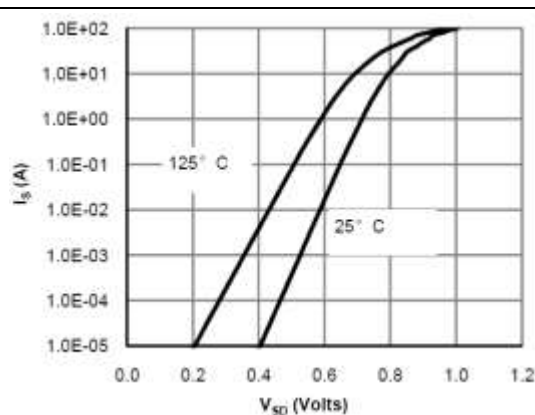


Figure 7. Gate-Charge Characteristics

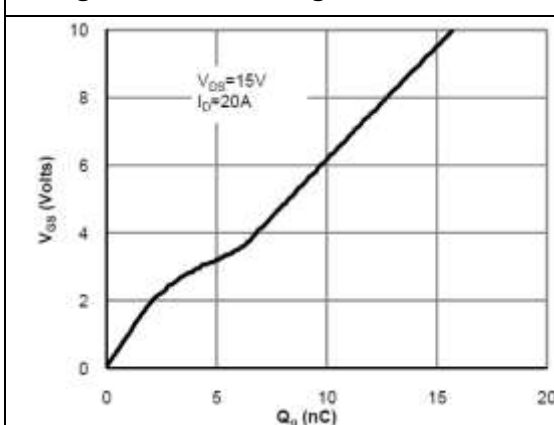


Figure 8. Capacitance Characteristics

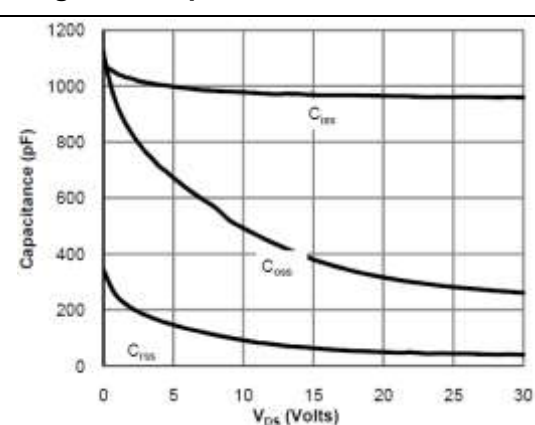


Figure 9. Maximum Forward Biased Safe Operating Area

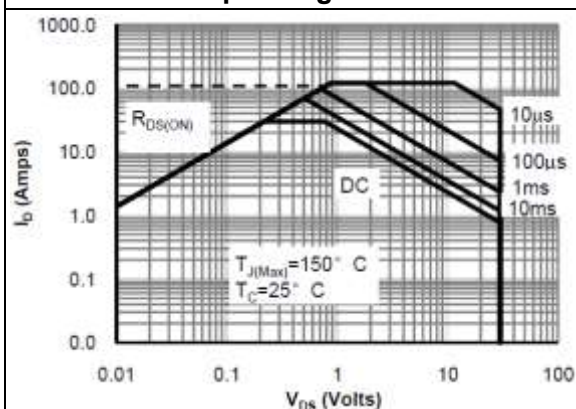


Figure 10. Single Pulse Power Rating Junction-to-Case

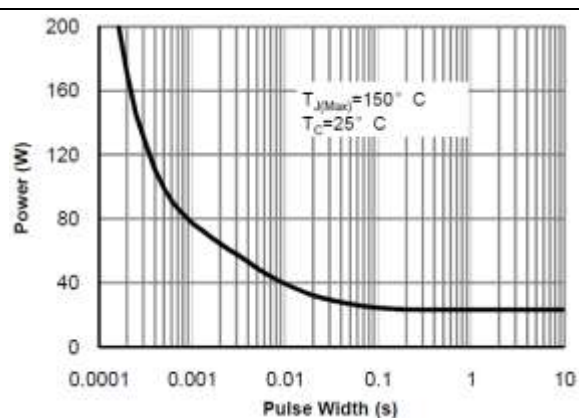
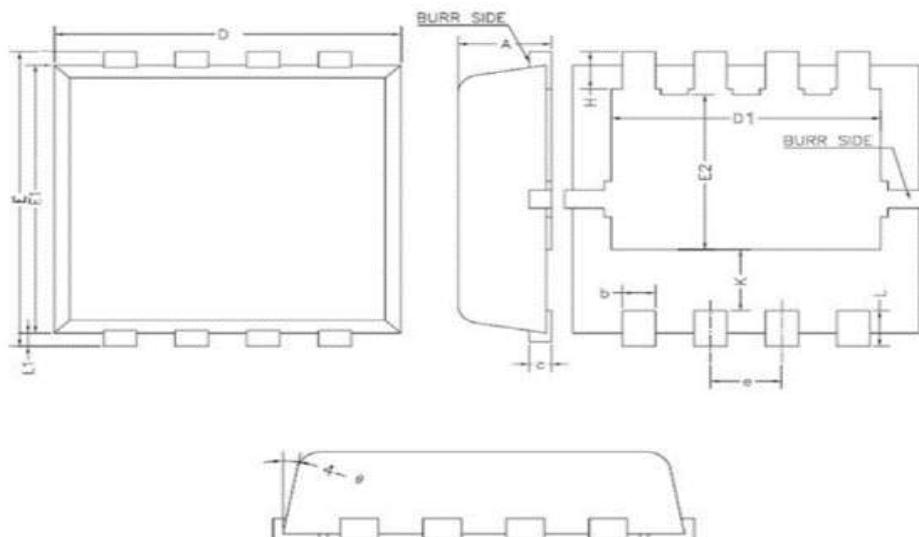
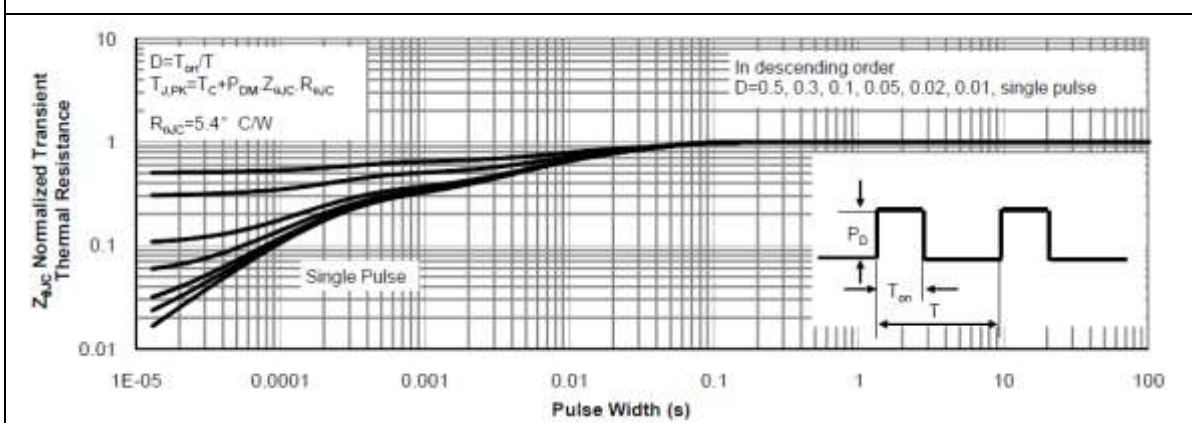


Figure 11. Normalized Maximum Transient Thermal Impedance



符号	尺寸 (mm)			符号	尺寸 (mm)		
	最小值	典型值	最大值		最小值	典型值	最大值
A	0.90	1.00	1.10	E1	5.70	5.75	5.80
b	0.33	0.41	0.51	E2	3.38	3.58	3.78
c	0.20	0.25	0.30	H	0.41	0.51	0.61
D	4.80	4.90	5.00	K	1.10	-	-
D1	3.61	3.81	3.96	L	0.51	0.61	0.71
e	1.27BSC			L1	0.06	0.13	0.20
E	5.90	6.00	6.10	theta	0°	-	12°

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