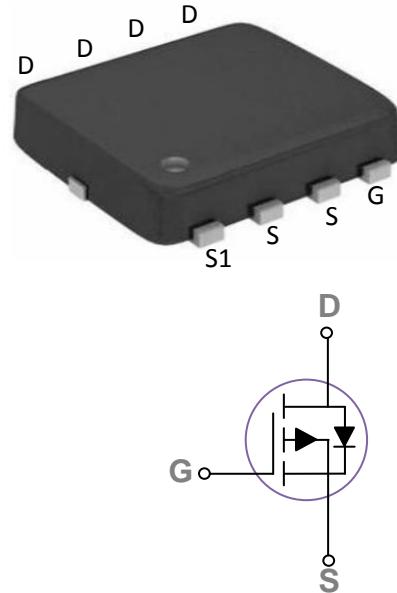


## Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=-40V, I_D=-38A, R_{DS(on)}=14m\Omega @ V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.



## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	-40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	-38	A
	Continuous Drain Current- $T_C=100^\circ C$	-24	
	Pulsed Drain Current <sup>1</sup>	-152	
$E_{AS}$	Single Pulse Avalanche Energy	130	mJ
$P_D$	Power Dissipation	52	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	°C

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{eJC}$	Thermal Resistance,Junction to Case	2.4	°C/W
$R_{eJA}$	Thermal Resistance,Junction to Ambient	62	

## Package Marking and Ordering Information:

Part NO.	Marking	Package
HAQ40P40	Q40P40	DFN3*3-8

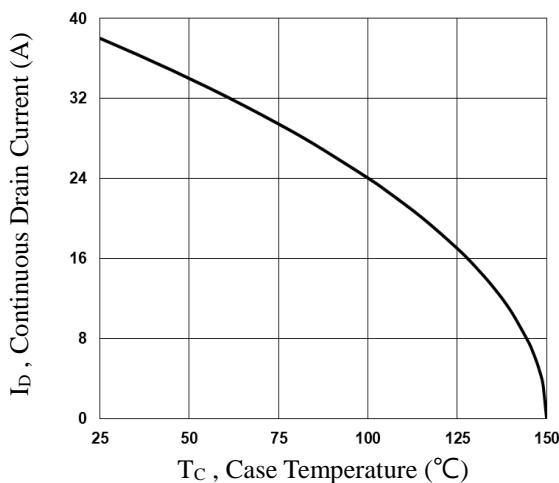
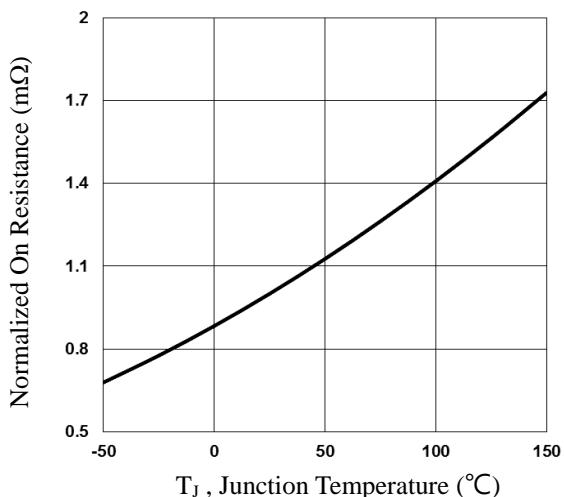
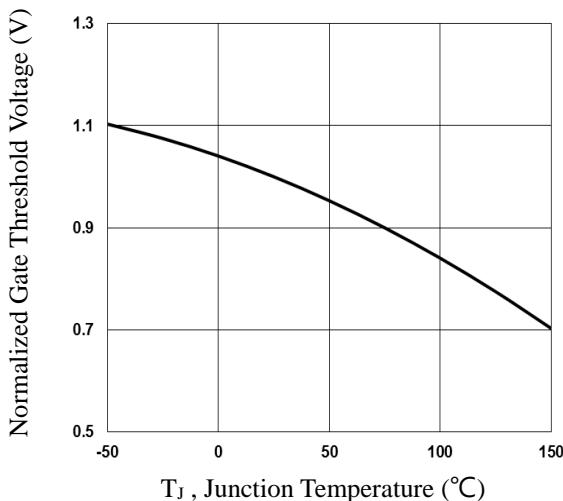
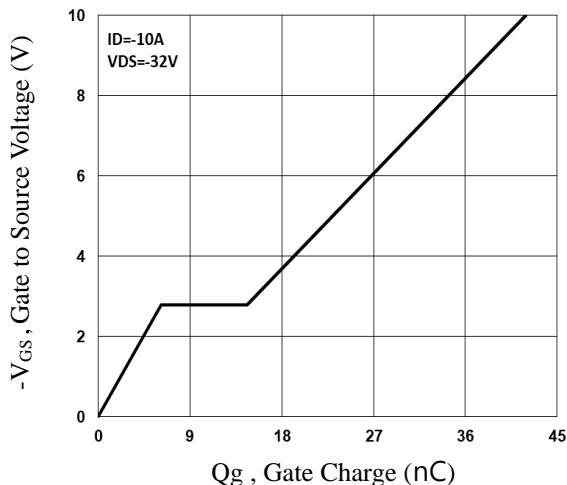
## Electrical Characteristics: ( $T_C=25^\circ\text{C}$ unless otherwise noted)

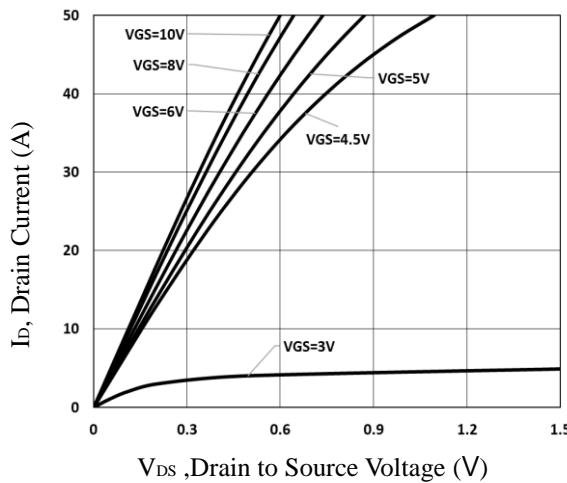
Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250 \mu\text{A}$	-40	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-40\text{V}$	---	---	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_D=250 \mu\text{A}$	-1	-1.6	-2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance	$V_{\text{GS}}=-10\text{V}, I_D=-15\text{A}$	---	11.3	14	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_D=-8\text{A}$	---	15.6	21	
$G_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}, I_D=-4\text{A}$	---	11	---	S
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	2757	4000	pF
$C_{\text{oss}}$	Output Capacitance		---	240	360	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	137	200	
<b>Switching Characteristics</b>						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=-20\text{V}, I_D=-\text{A}, R_{\text{GEN}}=6 \Omega, V_{\text{GS}}=-10\text{V}$	---	23	40	ns
$t_r$	Rise Time		---	10	20	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	135	250	ns
$t_f$	Fall Time		---	46	90	ns
$Q_g$	Total Gate Charge	$V_{\text{GS}}=-4.5\text{V}, V_{\text{DS}}=-32\text{V}, I_D=-10\text{A}$	---	22.2	40	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	8.2	16	nC
$Q_{\text{gd}}$	Gate-Drain "Miller" Charge		---	8.8	16	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Source-Drain Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}, I_S=-1\text{A}, T_j=25^\circ\text{C}$	---	---	-1	V

<b>I<sub>s</sub></b>	Diode Forward Current (Note 2)	·V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	-38	A
<b>I<sub>sm</sub></b>	Pulsed Source Current		---	-76	A

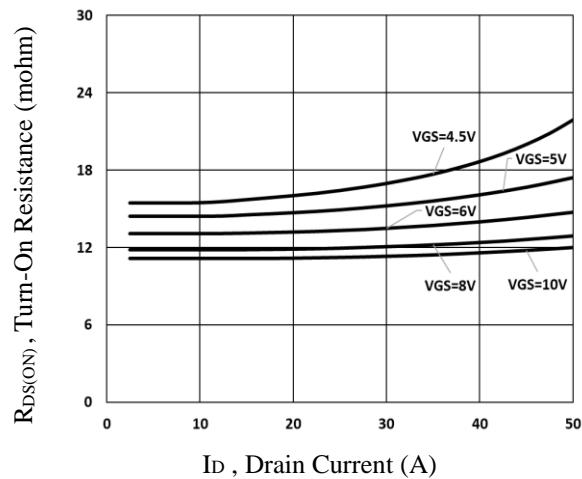
**Notes:**

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=51A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

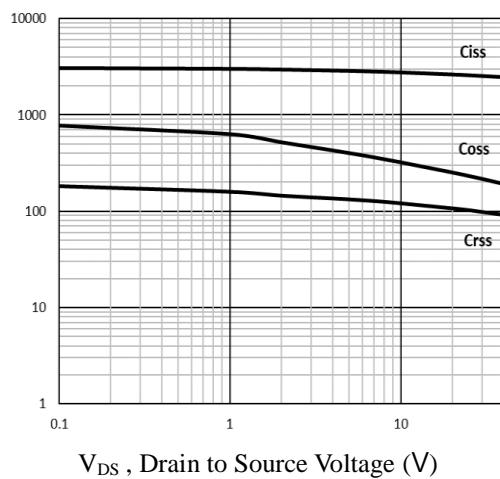
**Typical Characteristics:** (T<sub>c</sub>=25°C unless otherwise noted)

**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Normalized R<sub>DS(on)</sub> vs. T<sub>j</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Waveform**



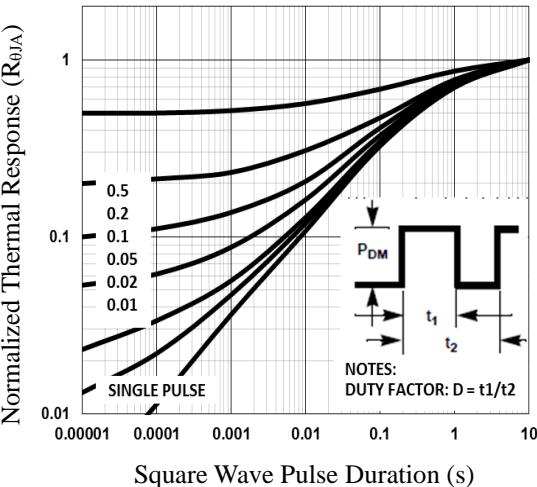
**Fig.5 Typical Output Characteristics**



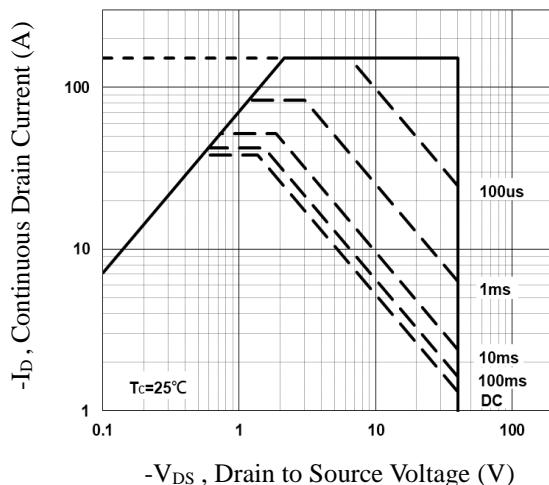
**Fig.6 Turn-On Resistance vs.  $I_D$**



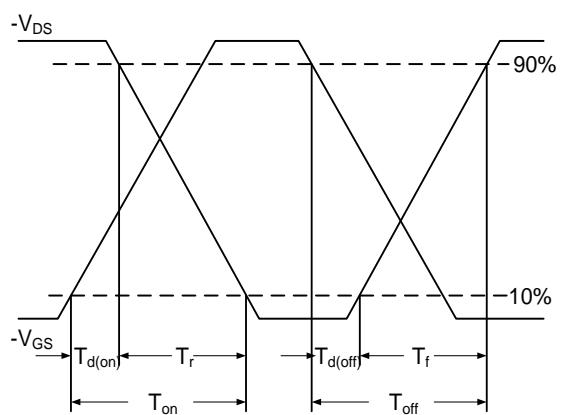
**Fig.7 Capacitance Characteristics**



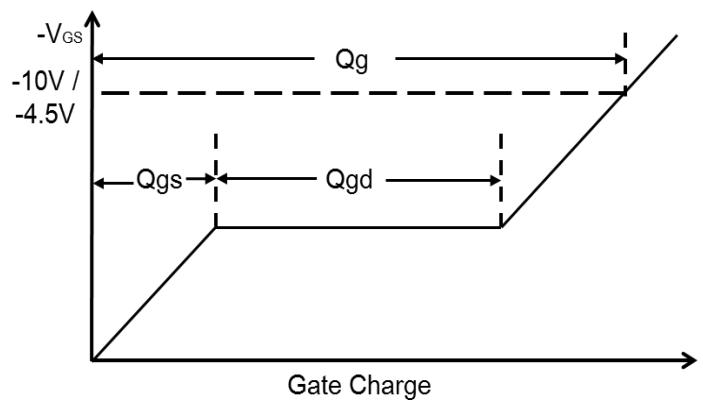
**Fig.8 Normalized Transient Impedance**



**Fig.9 Maximum Safe Operation Area**



**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**