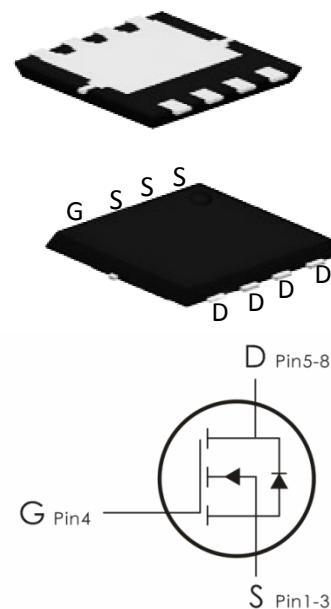


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=100V, I_D=40A, R_{DS(on)}<20m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $TC=25^\circ C$ ¹	40	A
	Continuous Drain Current- $TC=100^\circ C$	---	
	Pulsed Drain Current ²	120	
E_{AS}	Single Pulse Avalanche Energy ⁵	30	mJ
P_D	Power Dissipation ³	72	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	1.74	$^\circ C/W$
R_{eJA}	Thermal Resistance Junction to mbient ⁴	62	$^\circ C/W$

Package Marking and Ordering Information:

Part NO.	Marking	Package
HAN0140	N0140	DFN5*6

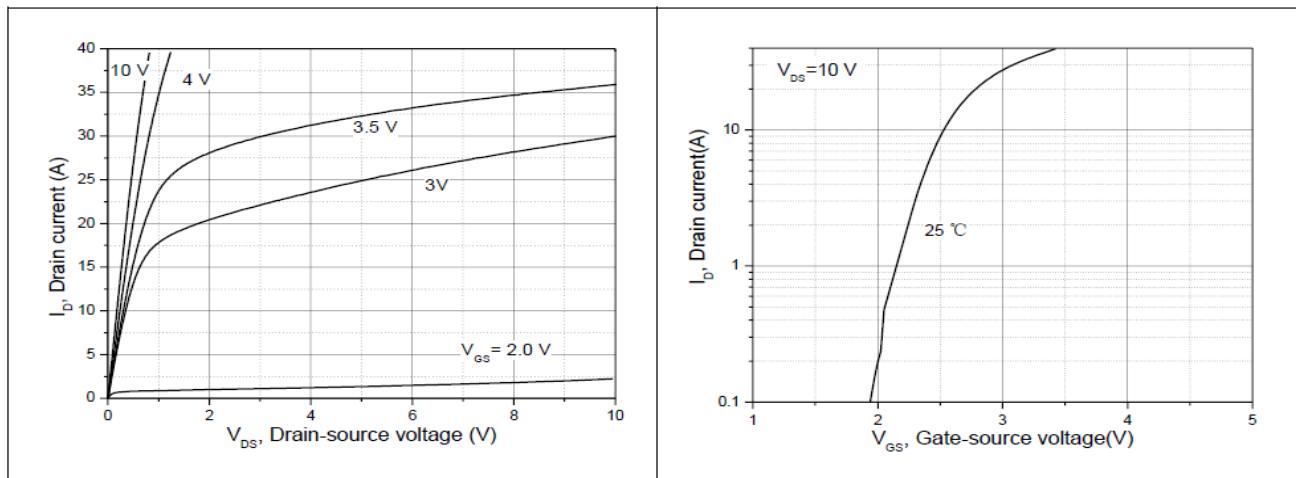
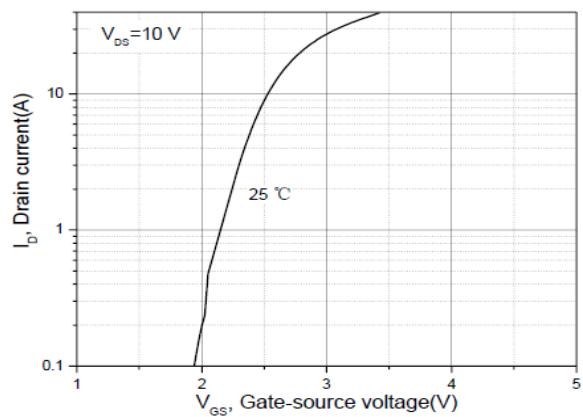
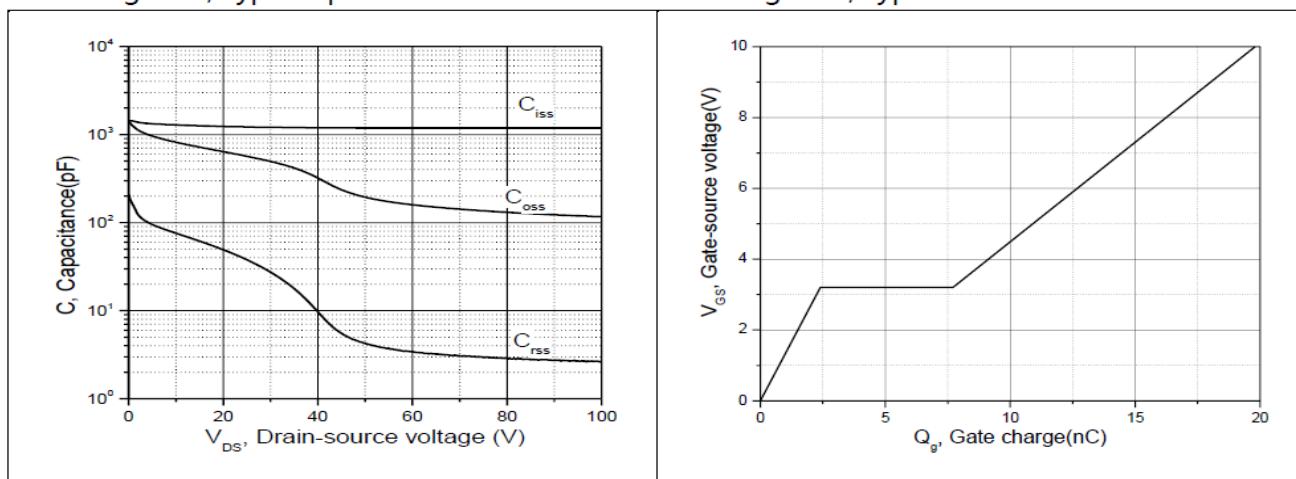
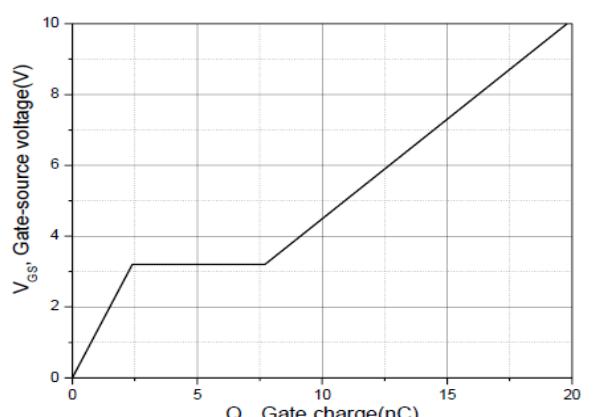
Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	100	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics³						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1	---	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=12\text{A}$	---	5.8	8	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=9\text{A}$	---	8.5	10	
Dynamic Characteristics⁴						
C_{iss}	Input Capacitance	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1190.6	---	pF
C_{oss}	Output Capacitance		---	194.6	---	
C_{rss}	Reverse Transfer Capacitance		---	4.1	---	
Switching Characteristics⁴						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}, I_{\text{D}}=10\text{A}, R_{\text{G}}=2.2\Omega$ $V_{\text{GS}}=10\text{V}$	---	17.8	---	ns
t_r	Rise Time		---	3.9	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		---	33.5	---	ns
t_f	Fall Time		---	3.2	---	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=50\text{V}, I_{\text{D}}=8\text{A}$	---	19.8	---	nC
Q_{gs}	Gate-Source Charge		---	2.4	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	5.3	---	nC
Drain-Source Diode Characteristics						
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{SD}	Source-Drain Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=8\text{A}$	---	---	1.3	V

LS	Continuous Source Current	VGS\<Vth	---	---	40	A
LSp	Pulsed Source Current		---	---	120	
Trr	Reverse Recovery Time	$I_S = 8 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$	---	50.2	---	NS
Qrr	Reverse Recovery Charge		---	95.1	---	NC

Notes:

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a=25^\circ\text{C}$.
- 5) $V_{DD}=50 \text{ V}$, $R_G=25 \Omega$, $L=0.3 \text{ mH}$, starting $T_j=25^\circ\text{C}$.

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Figure 1, Typ. output characteristics

Figure 2, Typ. transfer characteristics

Figure 3, Typ. capacitances

Figure 4, Typ. gate charge

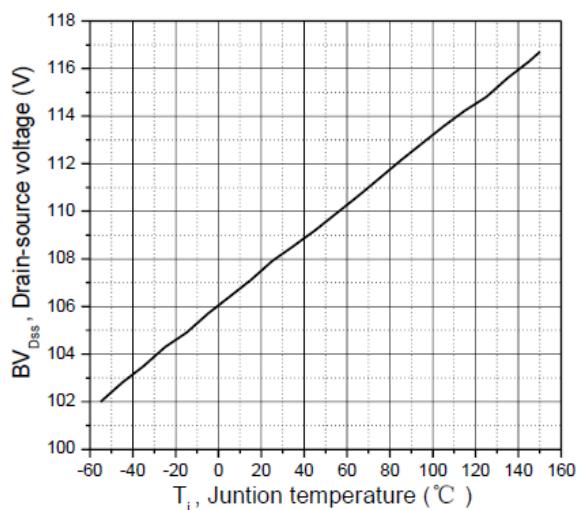


Figure 5, Drain-source breakdown voltage

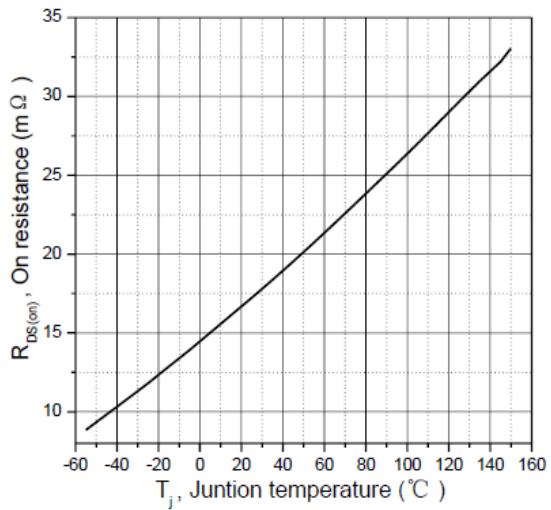


Figure 6, Drain-source on-state resistance

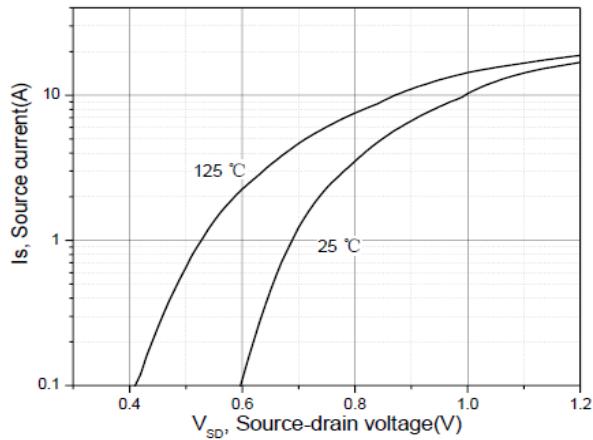


Figure 7, Forward characteristic of body diode

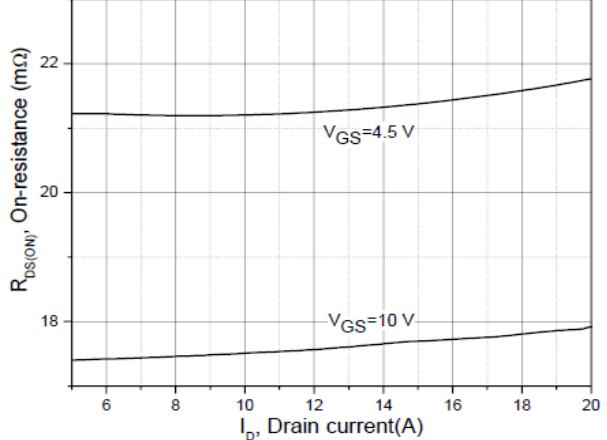


Figure 8, Drain-source on-state resistance

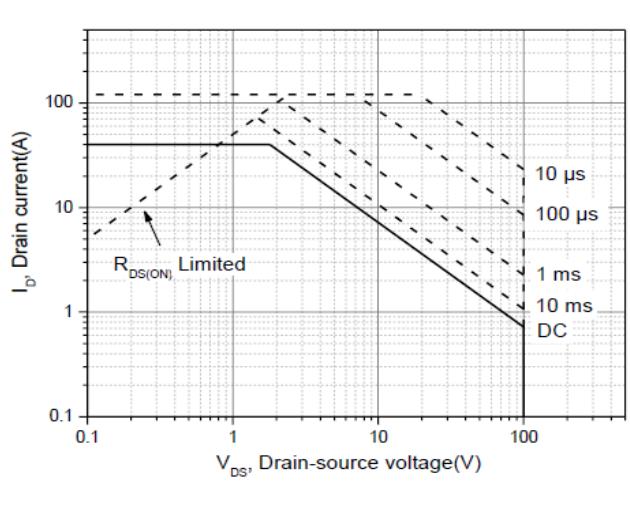


Figure 9, Safe operation area $T_C=25\text{ }^{\circ}\text{C}$