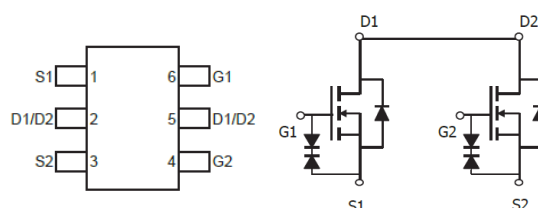
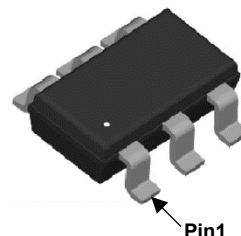


60V_{DS}/±20V_{GS} Dual N-Channel Power MOSFET

Features

- $V_{DS}=60V, I_D=0.5A$
- $R_{DS(ON)}=300m\Omega$ (TYP.) $V_{GS}=4.5V$
- $R_{DS(ON)}=400m\Omega$ (TYP.) $V_{GS}=2.5V$
- $R_{DS(ON)}=500m\Omega$ (TYP.) $V_{GS}=1.8V$
- ESD Rating:2KV HBM
- Low On-Resistance

SOT23-6L



Applications

- Switch switching
- Power management in portable/desktop PCs

Ordering Information

Device	package	Device Marking	Package Qty.
DMN5L06DMK	SOT-163	N5L06D	3000

Absolute Maximum Ratings ($T_C=25^{\circ}C$, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	60	V
Gate-Source Voltage ($V_{GS}=0V$, static)	V_{GS}	±20	V
Continuous Drain Current ($T_C=25^{\circ}C$)	I_D	0.5	A
Continuous Drain Current ($T_C=100^{\circ}C$)		0.2	A
Pulesd Drain Current	I_{DM}	25	A
Maximum Power Dissipation ($T_C=25^{\circ}C$)	P_D	1.4	W
Maximum Power Dissipation ($T_C=100^{\circ}C$)		0.9	W
Operating, Storage Temperature Range	T_J, T_{STG}	-55~150	$^{\circ}C$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	68	-	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	-	105	-	$^{\circ}C/W$

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$	-	-	1	μA
Gate -Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.4	0.8	1.1	V
Drain-Source On-stage Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=7A$	-	220	300	m Ω
		$V_{GS}=2.5V, I_D=5.5A$	-	260	400	
		$V_{GS}=1.8V, I_D=5A$	-	420	500	

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=10V$ $V_{GS}=0V$ $f=1MHz$	-	1200	-	pF
Output capacitance	C_{oss}		-	160	-	
Reverse transfer capacitance	C_{rss}		-	80	-	
Total Gate Charge	Q_g	$V_{DS}=10V$ $V_{GS}=4.5V$ $I_D=7A$	-	14	-	nC
Gate Source Charge	Q_{gs}		-	4.2	-	
Gate Drain Charge	Q_{gd}		-	2.6	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=4.5V$ $V_{DS}=10V$ $R_L=1.54\Omega$ $R_G=3\Omega$	-	270	-	ns
Rise time	t_r		-	320	-	
Turn-off delay Time	$t_{d(off)}$		-	3	-	
Fall time	t_f		-	2.2	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-	0.75	1	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=7A$ $dI/dt=100A/\mu s$	-	30	-	ns
Reverse Recovery Charge	Q_{rr}		-	6.5	-	nC

Electrical Characteristics Diagrames

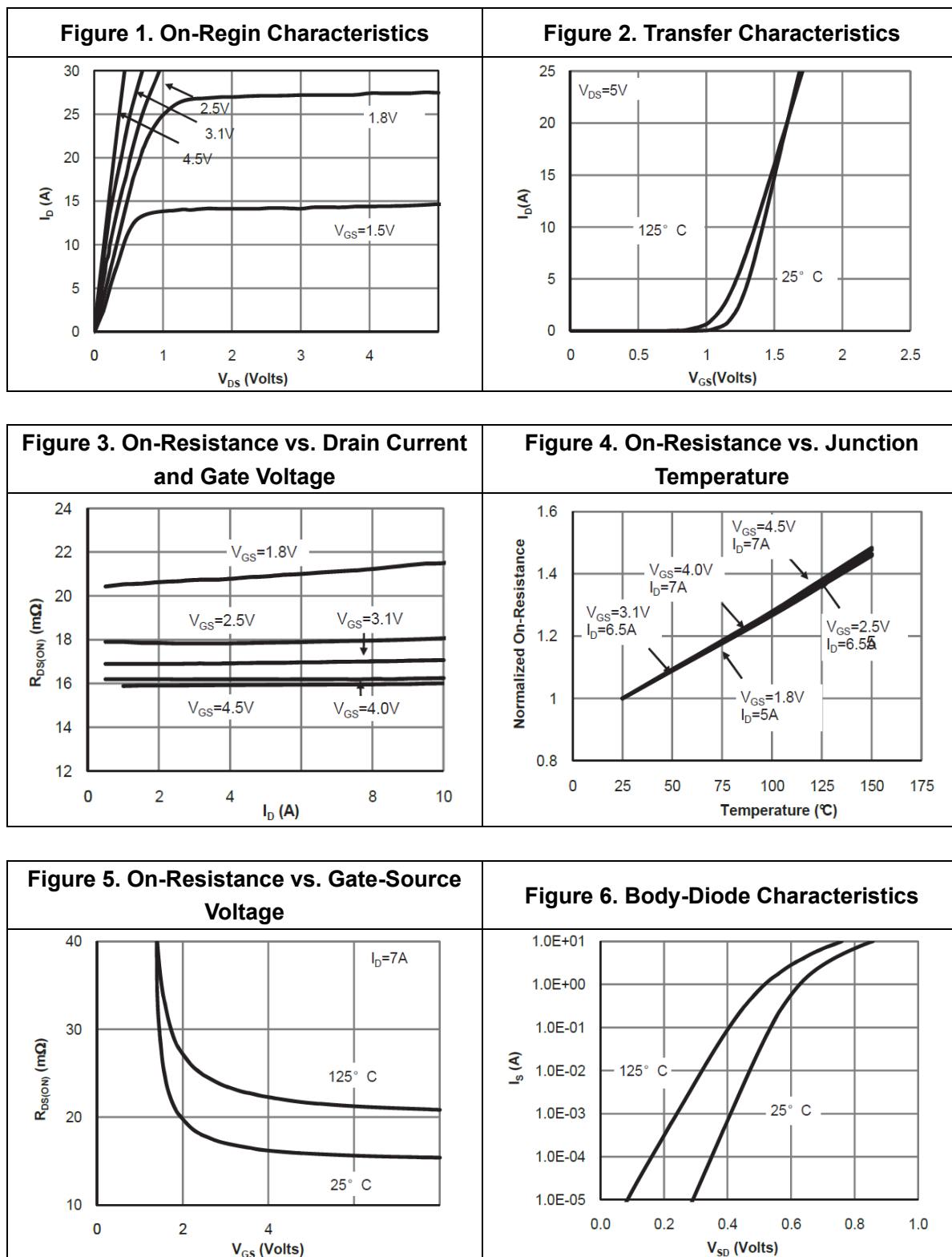


Figure 7. Gate-Charge Characteristics

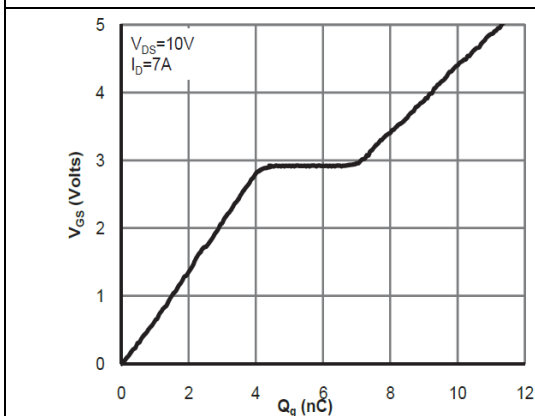


Figure 8. Capacitance Characteristics

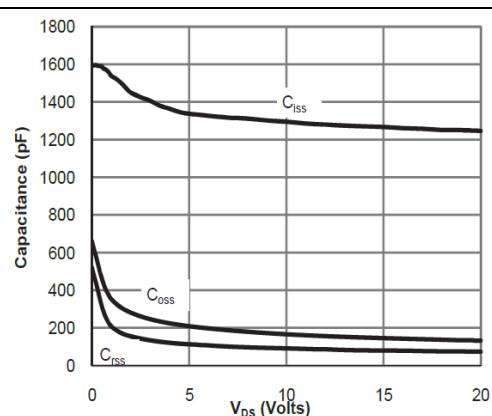


Figure 9. Maximum Forward Biased Safe Operating Area

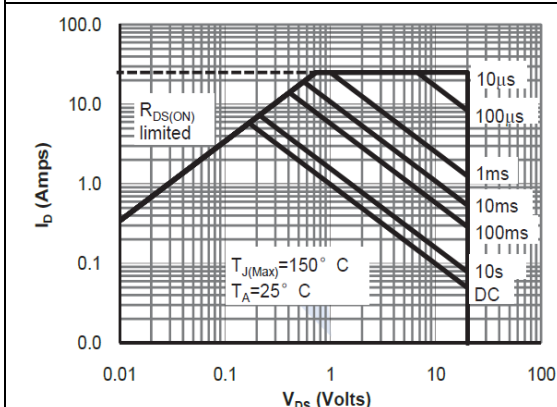


Figure 10. Single Pulse Power Rating Junction-to-Ambient

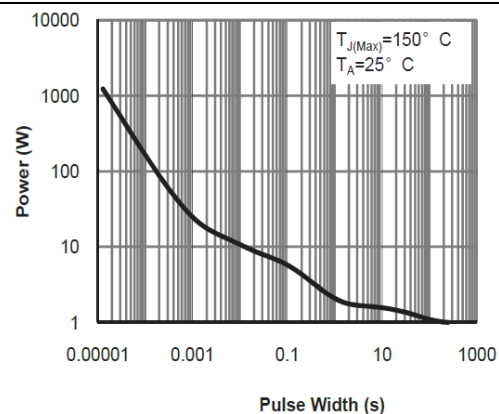
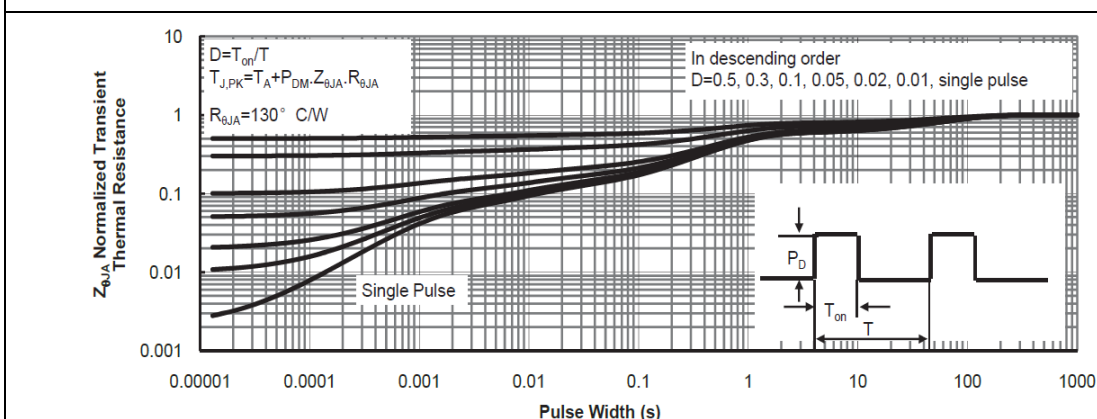
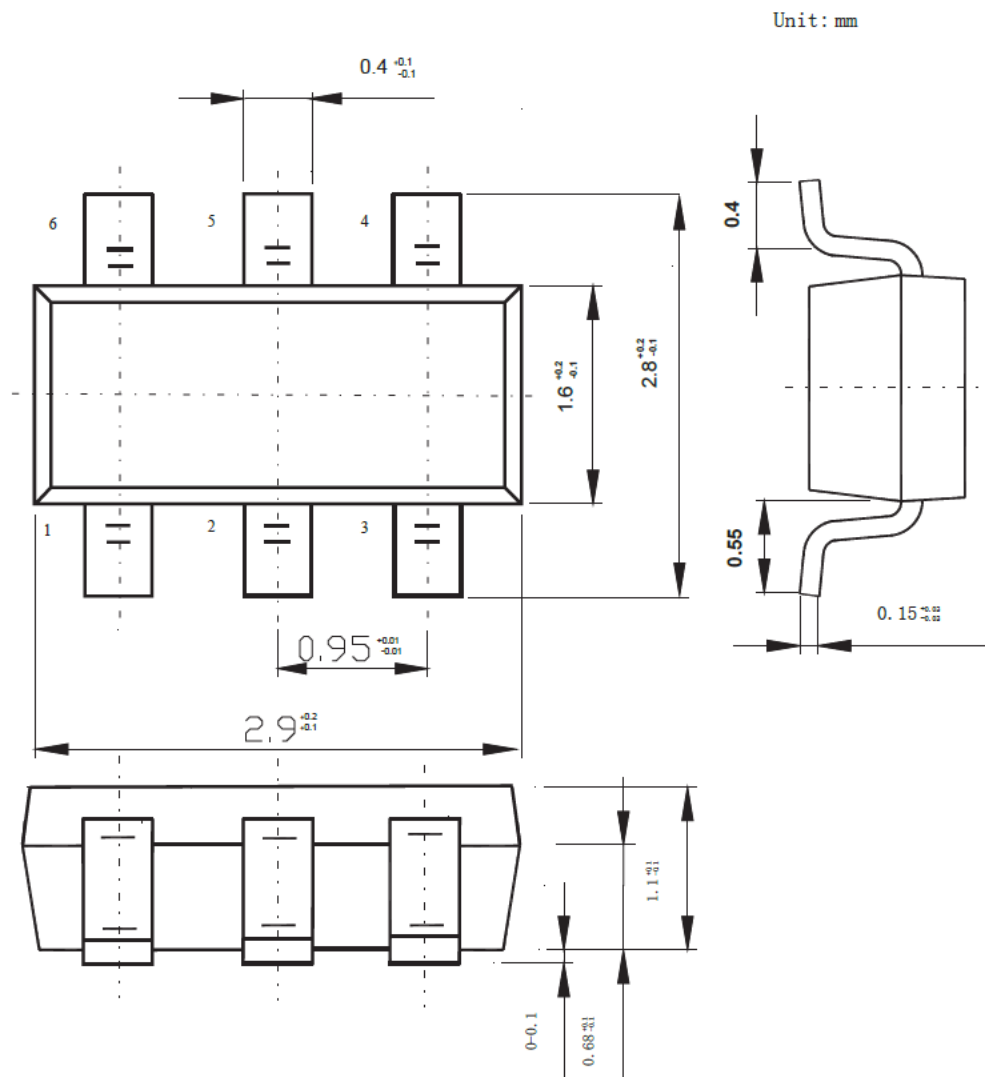


Figure 11. Normalized Maximum Transient Thermal Impedance



Physical Dimensions

SOT23-6L



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