

80V_{DS}/±20V_{GS} N-Channel Enhancement Mode MOSFET

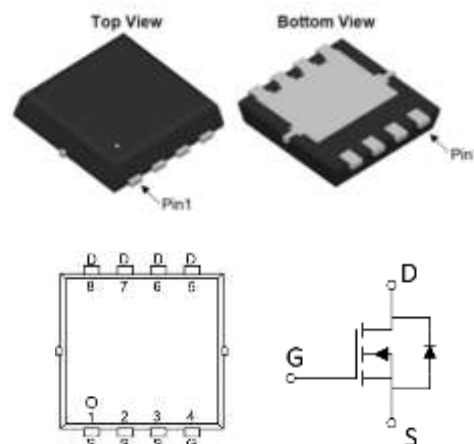
Features

- $V_{DS}=80V, I_D=100A$
- $R_{DS(ON)}=4.5m\Omega$ (TYP.) $V_{GS}=10V$
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability

Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

PDFN5060



Ordering Information

Device	package	Device Marking	Package Qty.
CJAC100SN08U	PDFN5060	**	5000/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	80	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	100	A
Continuous Drain Current (T _C =100°C)		63	A
Pulsed Drain Current	I _{DM}	400	A
Single Pulsed Avalanche Energy	E _{AS}	230	mJ
V _{DS} Spike 100ns	V _{SPIKE}	65	V
Maximum Power Dissipation (T _C =25°C)	P _D	132	W
Operating, Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	2.3	-	°C/W

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	80	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
Gate -Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	-	2.4	V
Drain-Source On-stage Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$	-	-	5.5	m Ω

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$	-	4738	-	pF
Output capacitance	C_{oss}	$V_{GS}=0V$	-	621	-	
Reverse transfer capacitance	C_{rss}	$f=1MHz$	-	15	-	
Gate Resistance	R_g	$f=1MHz$	-	1.5	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$	-	66	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=10V$	-	23	-	
Gate Drain Charge	Q_{gd}	$I_D=20A$	-	10	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$	-	20	-	ns
Rise time	t_r	$V_{DS}=15V$	-	79	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=0.75\Omega$	-	68	-	
Fall time	t_f	$R_G=3\Omega$	-	29	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-	0.7	1	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=20A$	-	50	-	ns
Reverse Recovery Charge	Q_{rr}	$d_i/d_t=500A/\mu s$	-	75	-	nC

Electrical Characteristics Diagrames

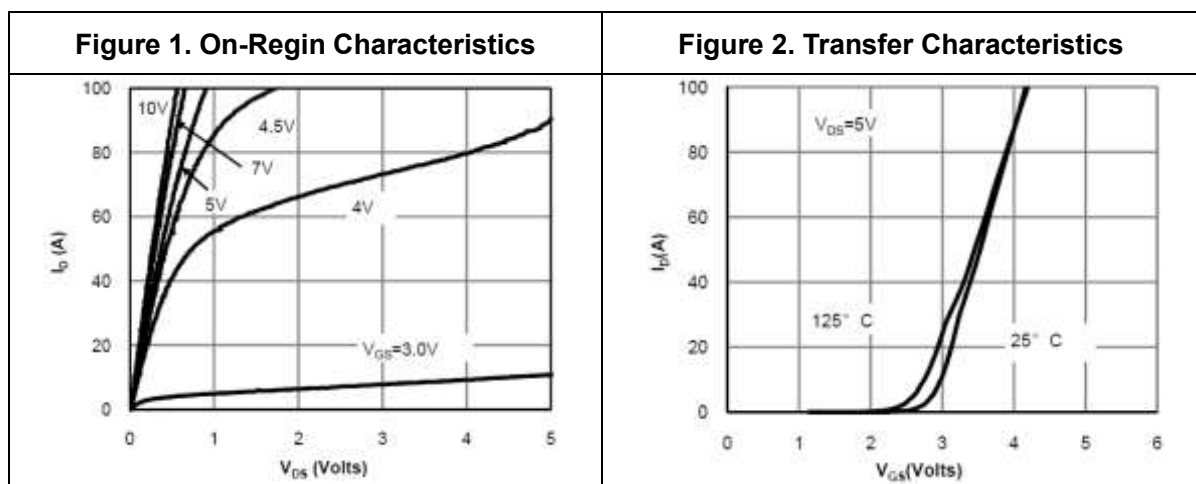


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

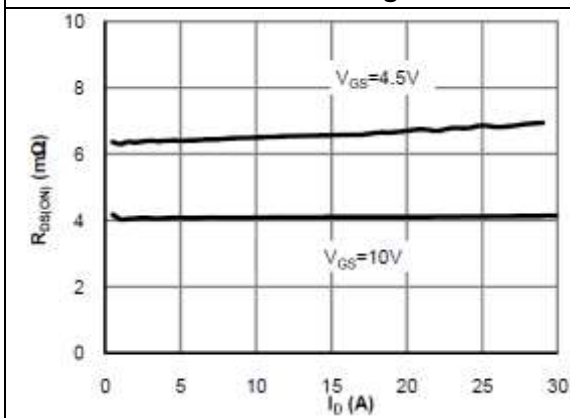


Figure 4. On-Resistance vs. Junction Temperature

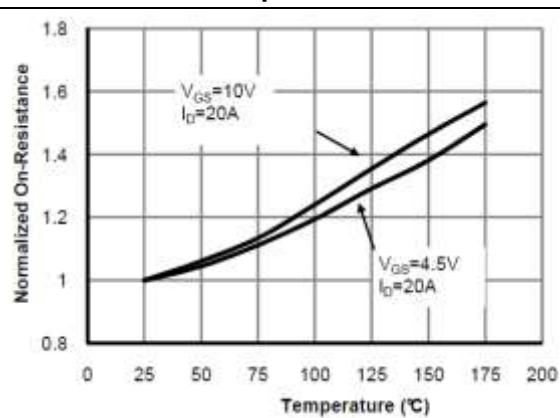


Figure 5. On-Resistance vs. Gate-Source Voltage

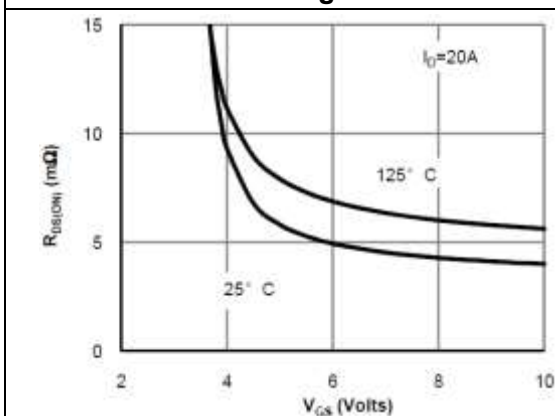


Figure 6. Body-Diode Characteristics

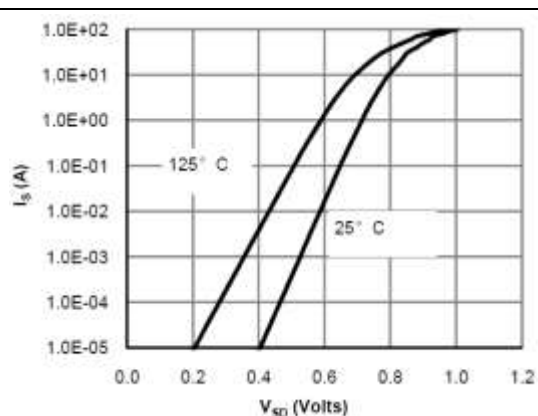


Figure 7. Gate-Charge Characteristics

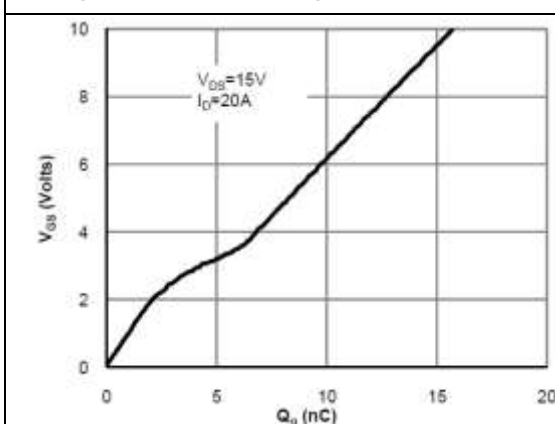


Figure 8. Capacitance Characteristics

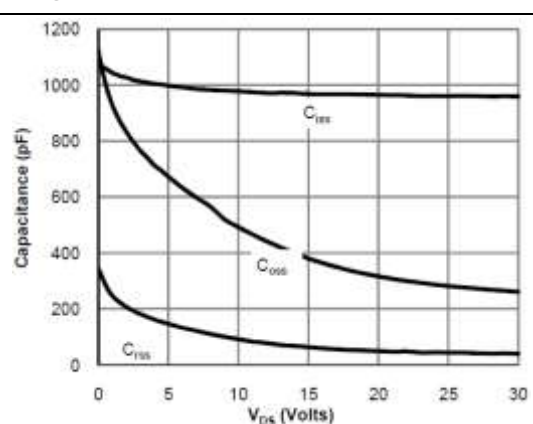


Figure 9. Maximum Forward Biased Safe Operating Area

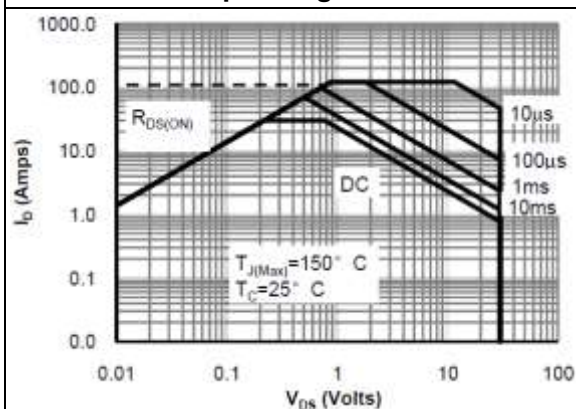


Figure 10. Single Pulse Power Rating Junction-to-Case

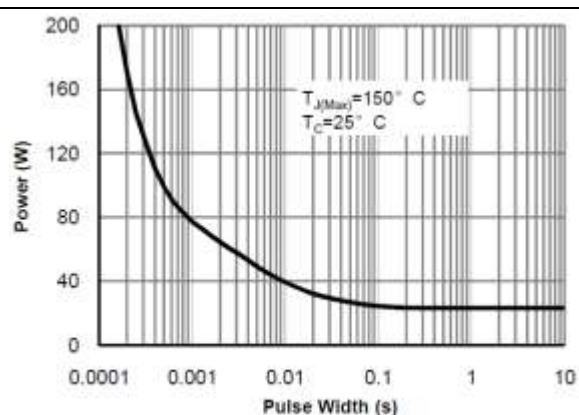
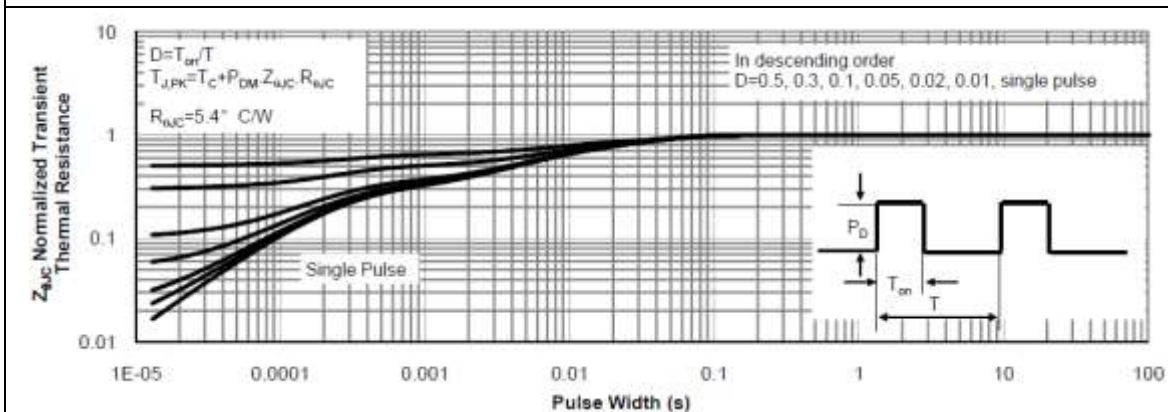


Figure 11. Normalized Maximum Transient Thermal Impedance



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