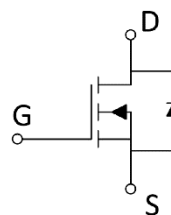
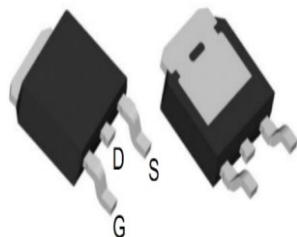


80V_{DS}/±20V_{GS} N-Channel Enhancement Mode MOSFET

Features

- V_{DS}=80V, I_D=55A
- R_{DS(ON)}=17mΩ (TYP.) V_{GS}=10V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability
- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

TO-252



Ordering Information

Device	package	Device Marking	Package Qty.
DMTH8012LK3Q	TO-252	**	2500/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	80	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	55	A
Continuous Drain Current (T _C =100°C)		30	A
Pulsed Drain Current	I _{DM}	180	A
Maximum Power Dissipation	P _D	70	W
Maximum Power Dissipation		0.4	W/°C
Single pulse avalanche energy	E _{as}	70	mJ
Operating, Storage Temperature Range	T _J , T _{STG}	-55~175	°C

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	80	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1		3	V
Drain-Source On-state Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	14	19	mΩ

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	2.5	-	°C/W

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$ $V_{GS}=0V$ $f=1MHz$	-	1135	-	pF
Output capacitance	C_{oss}		-	390	-	
Reverse transfer capacitance	C_{rss}		-	18	-	
Gate Resistance	R_g	$f=1MHz$	-	1.5	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$ $V_{GS}=10V$ $I_D=20A$	-	16	-	nC
Gate Source Charge	Q_{gs}		-	5.6	-	
Gate Drain Charge	Q_{gd}		-	2.4	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$ $V_{DS}=15V$ $R_L=0.75\Omega$ $R_G=3\Omega$	-	39.2	-	ns
Rise time	t_r		-	11	-	
Turn-off delay Time	$t_{d(off)}$		-	53.2	-	
Fall time	t_f		-	15.8	-	
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-	0.85	1.2	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=20A$ $dI/dt=500A/\mu s$	-	22	-	ns
Reverse Recovery Charge	Q_{rr}		-	11	-	nC

Electrical Characteristics Diagrames

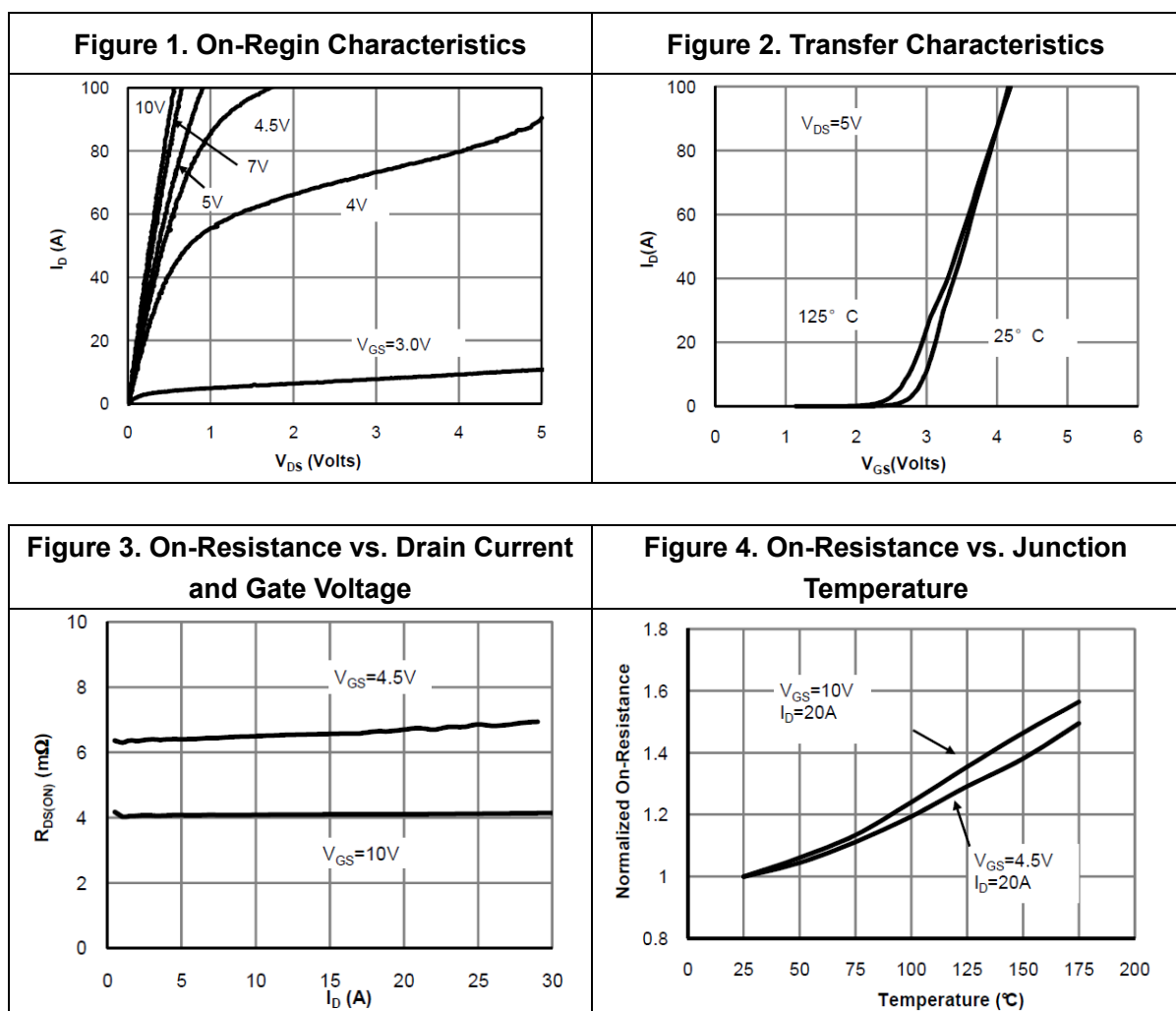
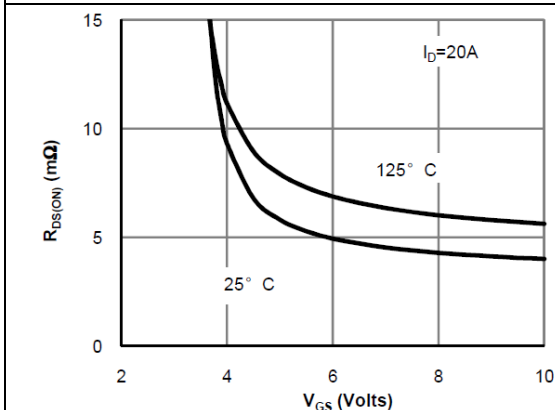
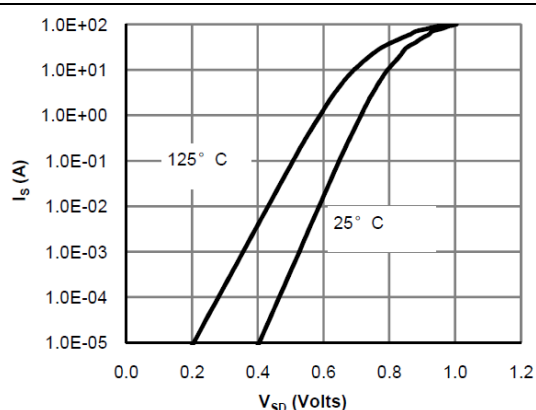
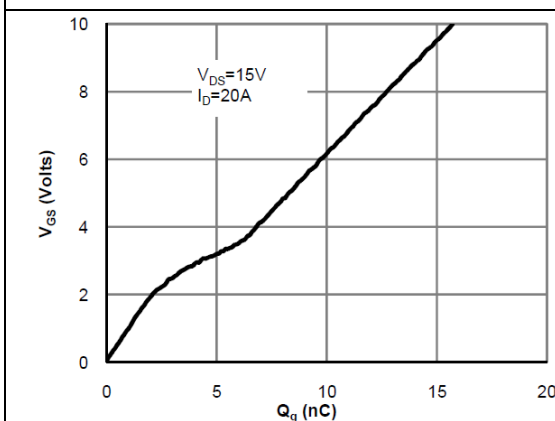
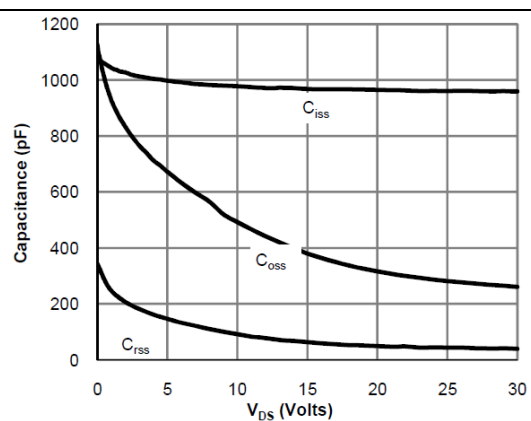
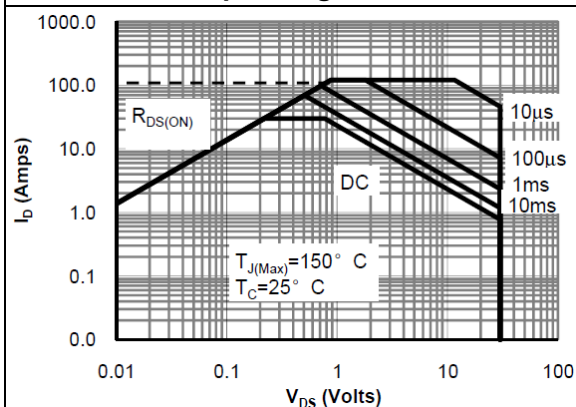
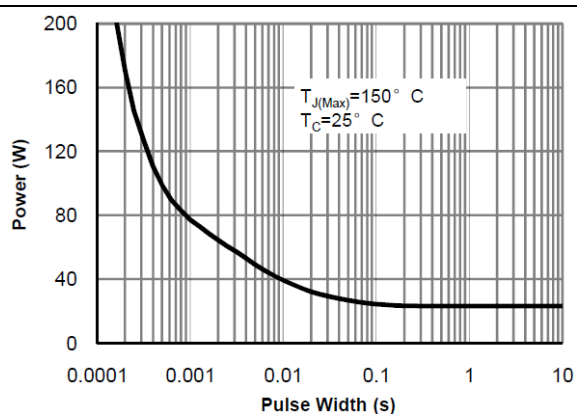


Figure 5. On-Resistance vs. Gate-Source Voltage

Figure 6. Body-Diode Characteristics

Figure 7. Gate-Charge Characteristics

Figure 8. Capacitance Characteristics

Figure 9. Maximum Forward Biased Safe Operating Area

Figure 10. Single Pulse Power Rating Junction-to-Case


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