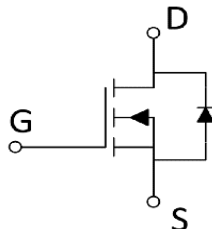
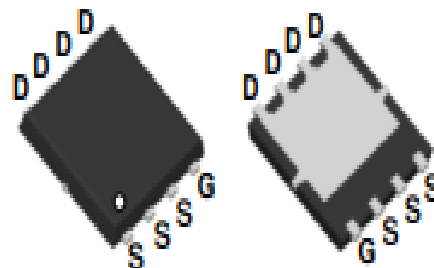


60V_{DS}/±20V_{GS} N-Channel Enhancement Mode MOSFET
Features

- V_{DS}=60V, I_D=50A
- R_{DS(ON)}=12mΩ (TYP.) V_{GS}=10V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability


PDFN3*3

Applications

- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

Ordering Information

Device	package	Device Marking	Package Qty.
FKBB6054	PDFN3*3	**	5000/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	60	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±20	V
Continuous Drain Current (T _C =25°C)	I _D	50	A
Continuous Drain Current (T _C =100°C)		26	A
Pulsed Drain Current	I _{DM}	80	A
Single Pulsed Avalanche Energy	E _{AS}	150	mJ
Maximum Power Dissipation (T _C =25°C)	P _D	35	W
Operating, Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	3.6	-	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	-	-	-	°C/W

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1	1.8	2.5	V
Drain-Source On-stage Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	12	18	mΩ
		V _{GS} =4.5V, I _D =20A	-	18	23	

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=15V$	-	2050	-	pF
Output capacitance	C_{oss}	$V_{GS}=0V$	-	158	-	
Reverse transfer capacitance	C_{rss}	$f=1MHz$	-	120	-	
Gate Resistance	R_g	$f=1MHz$	-	1.5	-	Ω
Total Gate Charge	Q_g	$V_{DS}=15V$	-	50	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=10V$	-	6	-	
Gate Drain Charge	Q_{gd}	$I_D=20A$	-	15	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=10V$	-	7.4	-	ns
Rise time	t_r	$V_{DS}=15V$	-	5.1	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=0.75\Omega$	-	28.2	-	
Fall time	t_f	$R_G=3\Omega$	-	5.5	-	
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=1A$	-		1.2	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_{SD}=20A$	-	28		ns
Reverse Recovery Charge	Q_{rr}	$d_i/d_t=500A/\mu s$	-	40		nC

Electrical Characteristics Diagrames

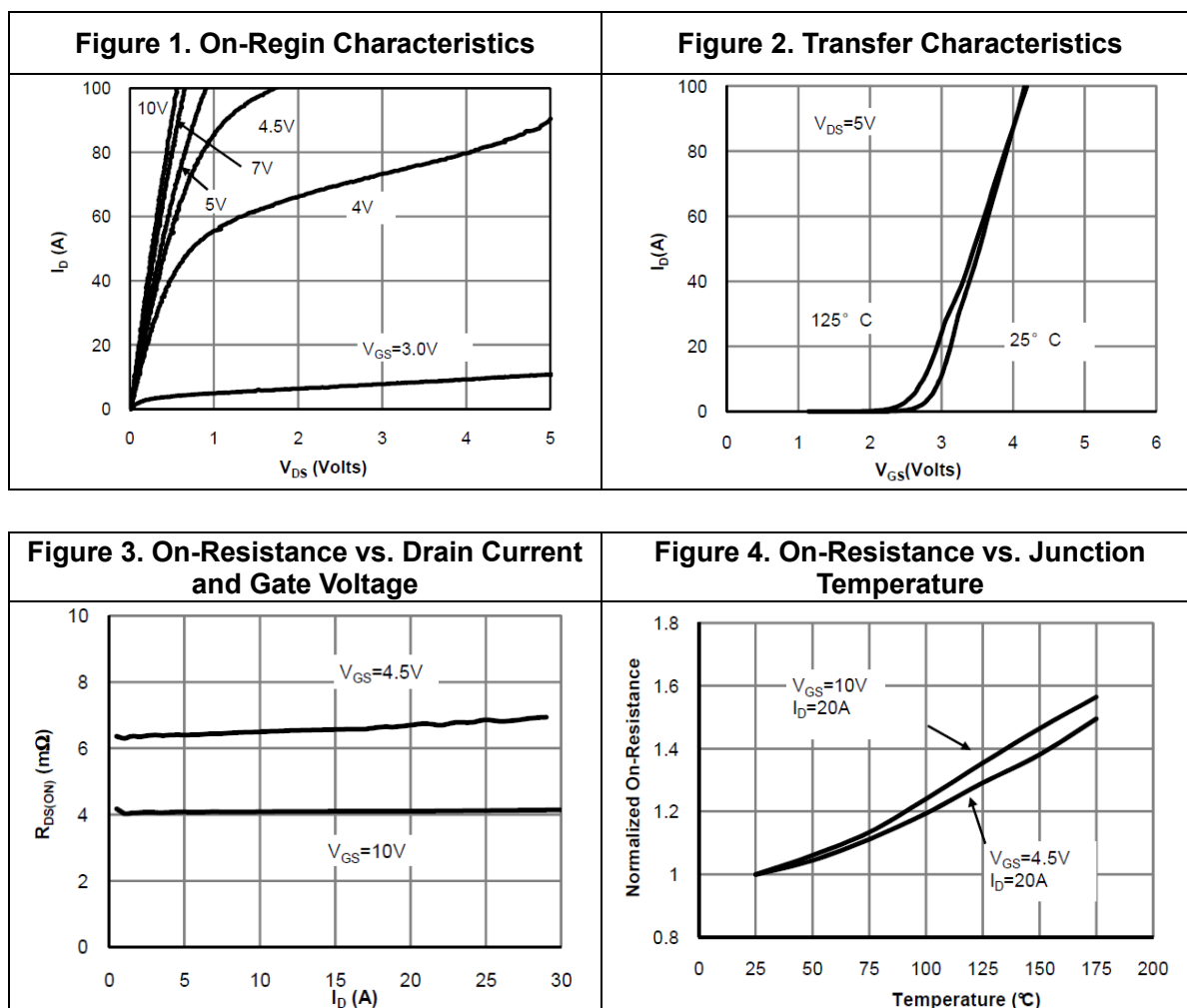
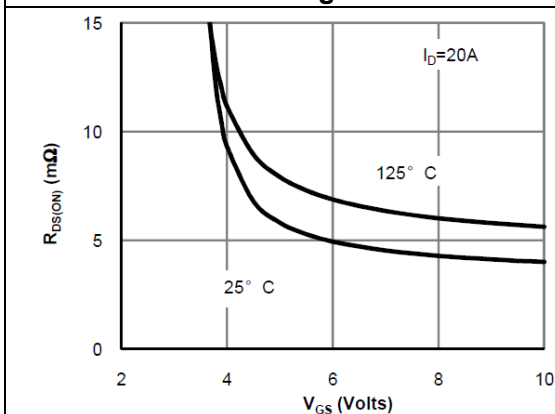
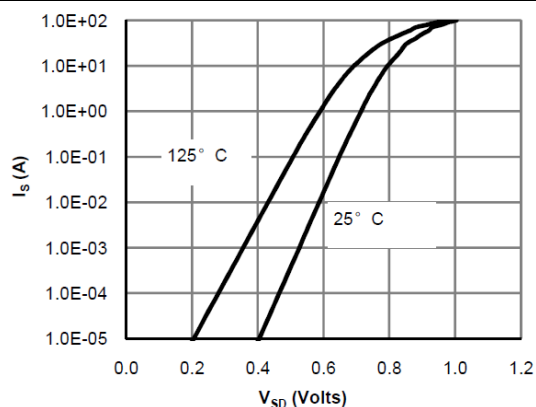
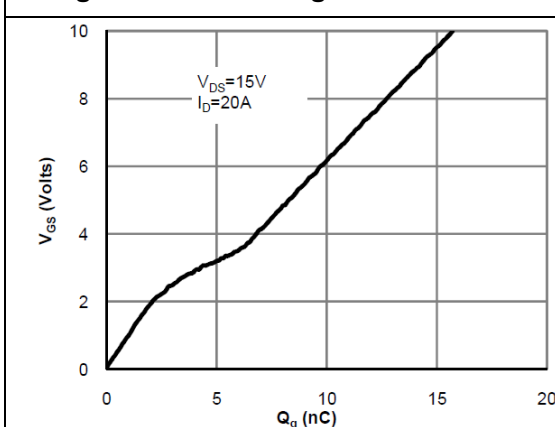
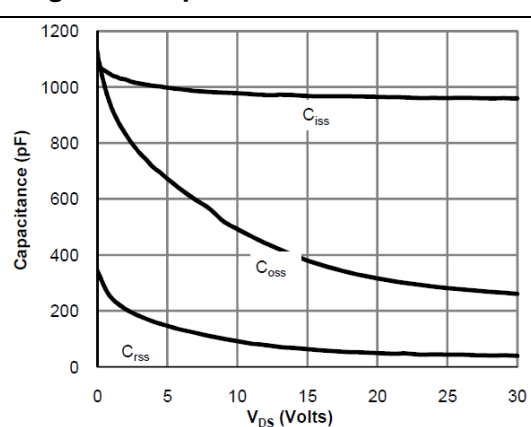
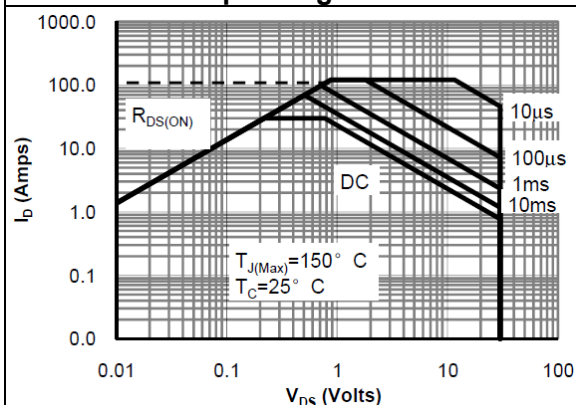
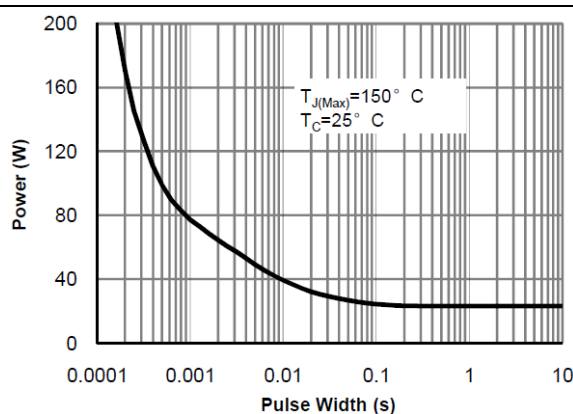


Figure 5. On-Resistance vs. Gate-Source Voltage

Figure 6. Body-Diode Characteristics

Figure 7. Gate-Charge Characteristics

Figure 8. Capacitance Characteristics

Figure 9. Maximum Forward Biased Safe Operating Area

Figure 10. Single Pulse Power Rating Junction-to-Case


< Copyright >

All the Patent, Copyright and IP contained in this document belong to HAMOS, shall not be reproduced, copied, or used in other ways without permission.