

-20V_{DS}/±12V_{GS} P-Channel Enhancement Mode MOSFET

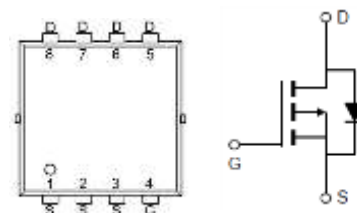
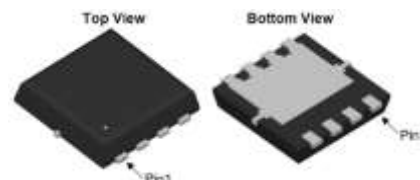
Features

- V_{DS}=-20V, I_D=-85A
- R_{DS(ON)}=3mΩ (TYP.) V_{GS}=-4.5V
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance

Applications

- Portable device
- Switch switching
- Load Switch
- Power management in portable/desktop PCs
- DC/DC conversion

PDFN5060



Ordering Information

Device	package	Device Marking	Package Qty.
HM20P85D5	PDFN5060	**	5000/PCS

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	-20	V
Gate-Source Voltage (V _{GS} =0V, static)	V _{GS}	±12	V
Continuous Drain Current (T _C =25°C)	I _D	-85	A
Continuous Drain Current (T _C =100°C)		-55	A
Pulsed Drain Current	I _{DM}	-340	A
Avalanche Energy, Single Pulsed	E _{AS}	154	mJ
Maximum Power Dissipation (T _C =25°C)	P _D	33	W
Operating, Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-	-1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =-250μA	-0.4	--	-1.0	V
Drain-Source On-stage Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-1A	-	-	3.0	mΩ
		V _{GS} =-2.5V, I _D =-1A	-	-	5	

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	-	3.8	-	$^{\circ}\text{C}/\text{W}$
Thermal Resistance,Junction-to-Ambient	$R_{\theta JA}$	-	-	-	$^{\circ}\text{C}/\text{W}$

Dynamic Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$V_{DS}=-15\text{V}$	-	1500	-	pF
Output capacitance	C_{oss}	$V_{GS}=0\text{V}$	-	1000	-	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$	-	-	-	
Gate Resistance	R_g	$f=1\text{MHz}$	-	-	-	Ω
Total Gate Charge	Q_g	$V_{DS}=-15\text{V}$	-	100	-	nC
Gate Source Charge	Q_{gs}	$V_{GS}=-10\text{V}$	-	21	-	
Gate Drain Charge	Q_{gd}	$I_D=-15\text{A}$	-	32	-	
Turn-on delay Time	$t_{d(on)}$	$V_{GS}=-10\text{V}$	-	50	-	ns
Rise time	t_r	$V_{DS}=-15\text{V}$	-	100	-	
Turn-off delay Time	$t_{d(off)}$	$R_L=1\Omega$	-	40	-	
Fall time	t_f	$R_G=3\Omega$	-	-	-	

Reverse Diode Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_{SD}=-1\text{A}$	-	-	-1.2	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0\text{V}, I_{SD}=-15\text{A}$	-	-	-	ns
Reverse Recovery Charge	Q_{rr}	$dI/dt=100\text{A}/\mu\text{s}$	-	-	-	nC

Electrical Characteristics Diagrames

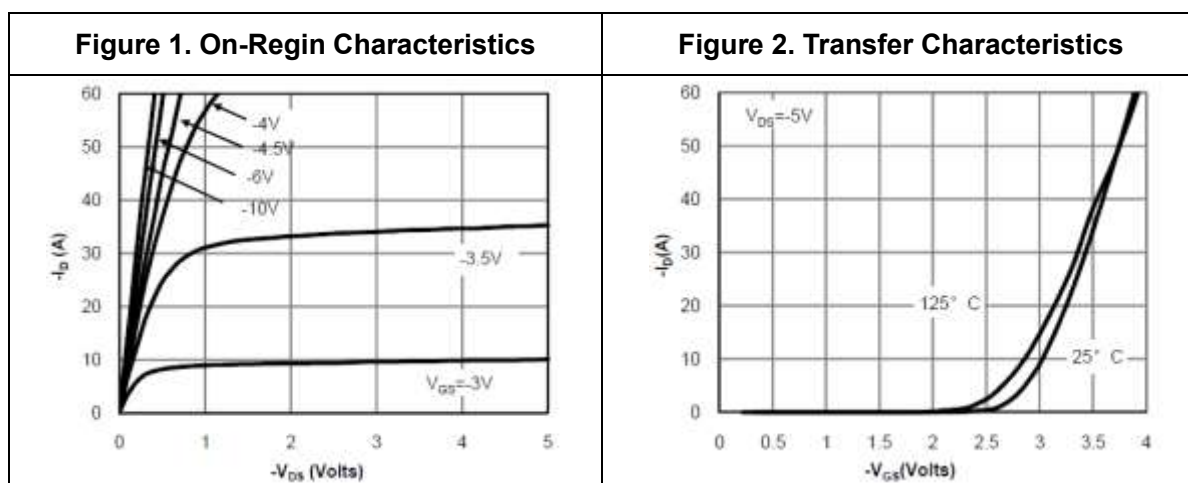


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

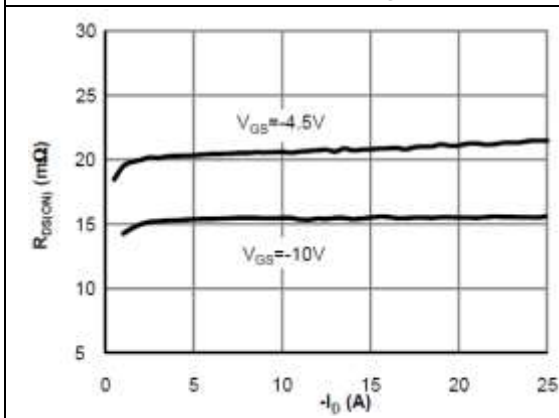


Figure 4. On-Resistance vs. Junction Temperature

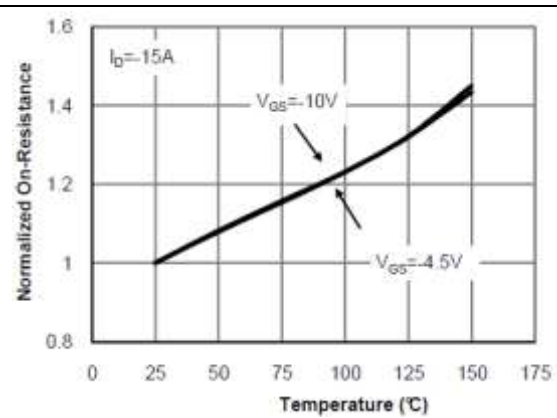


Figure 5. On-Resistance vs. Gate-Source Voltage

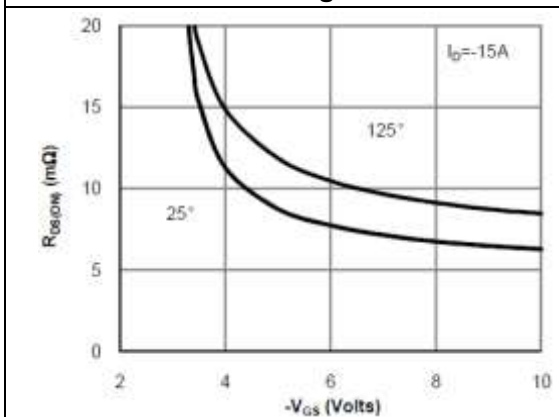


Figure 6. Body-Diode Characteristics

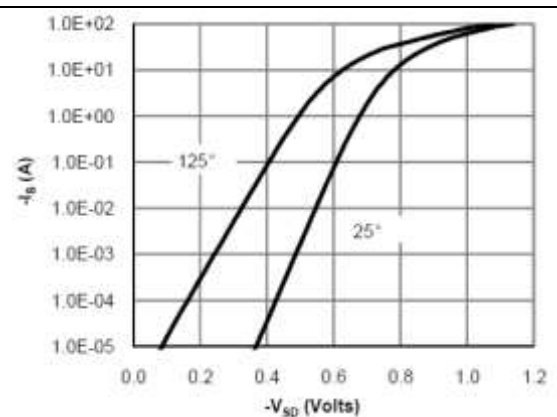


Figure 7. Gate-Charge Characteristics

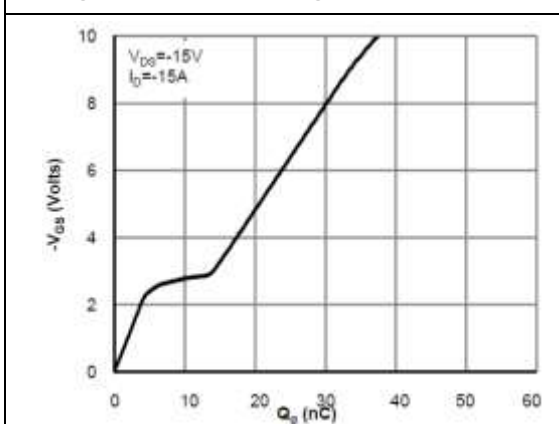


Figure 8. Capacitance Characteristics

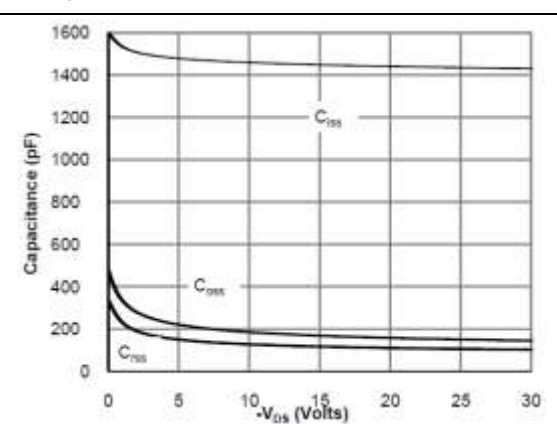


Figure 9. Maximum Forward Biased Safe Operating Area

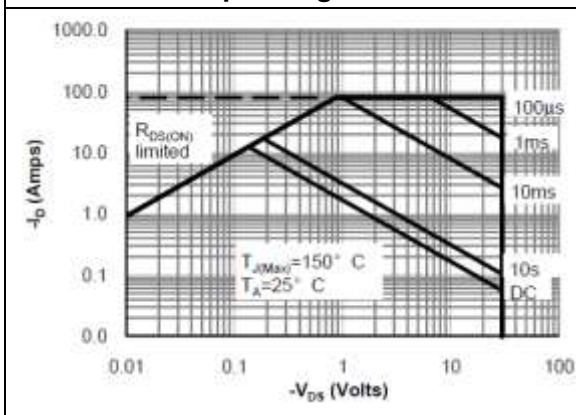


Figure 10. Single Pulse Power Rating Junction-to-Ambient

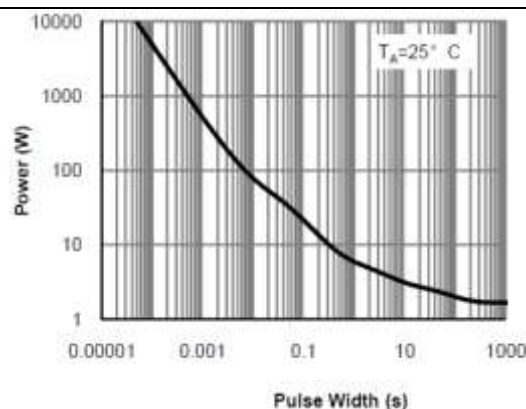
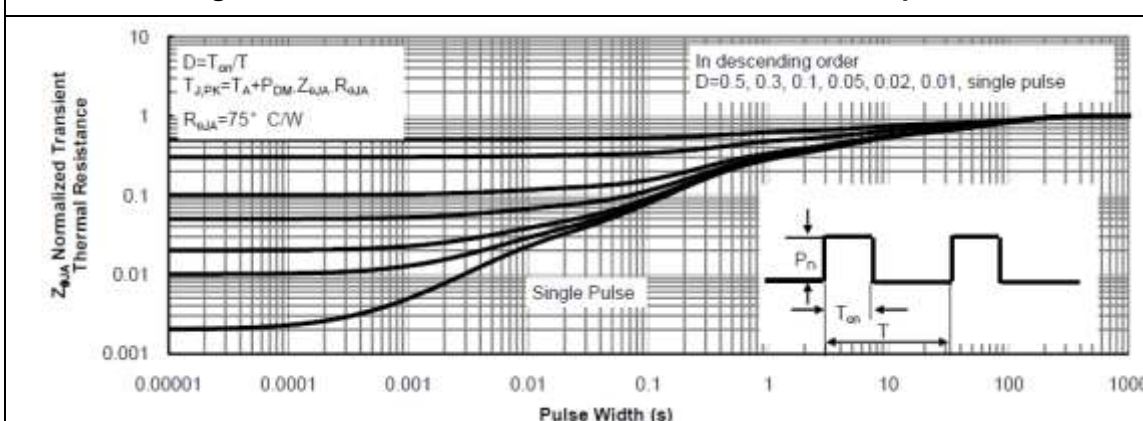


Figure 11. Normalized Maximum Transient Thermal Impedance



Physical Dimensions

PDFN5060

符号	尺寸 (mm)			符号	尺寸 (mm)		
	最小值	典型值	最大值		最小值	典型值	最大值
A	0.90	1.00	1.10	E1	5.70	5.75	5.80
b	0.33	0.41	0.51	E2	3.38	3.58	3.78
c	0.20	0.25	0.30	H	0.41	0.51	0.61
D	4.80	4.90	5.00	K	1.10	-	-
D1	3.61	3.81	3.96	L	0.51	0.61	0.71
e	1.27BSC			L1	0.06	0.13	0.20
E	5.90	6.00	6.10	θ	0°	-	12°

< Copyright >

All the Patent, Copyright and IP contained in this document belong to HAMOS, shall not be reproduced , copied, or used in other ways without permission.